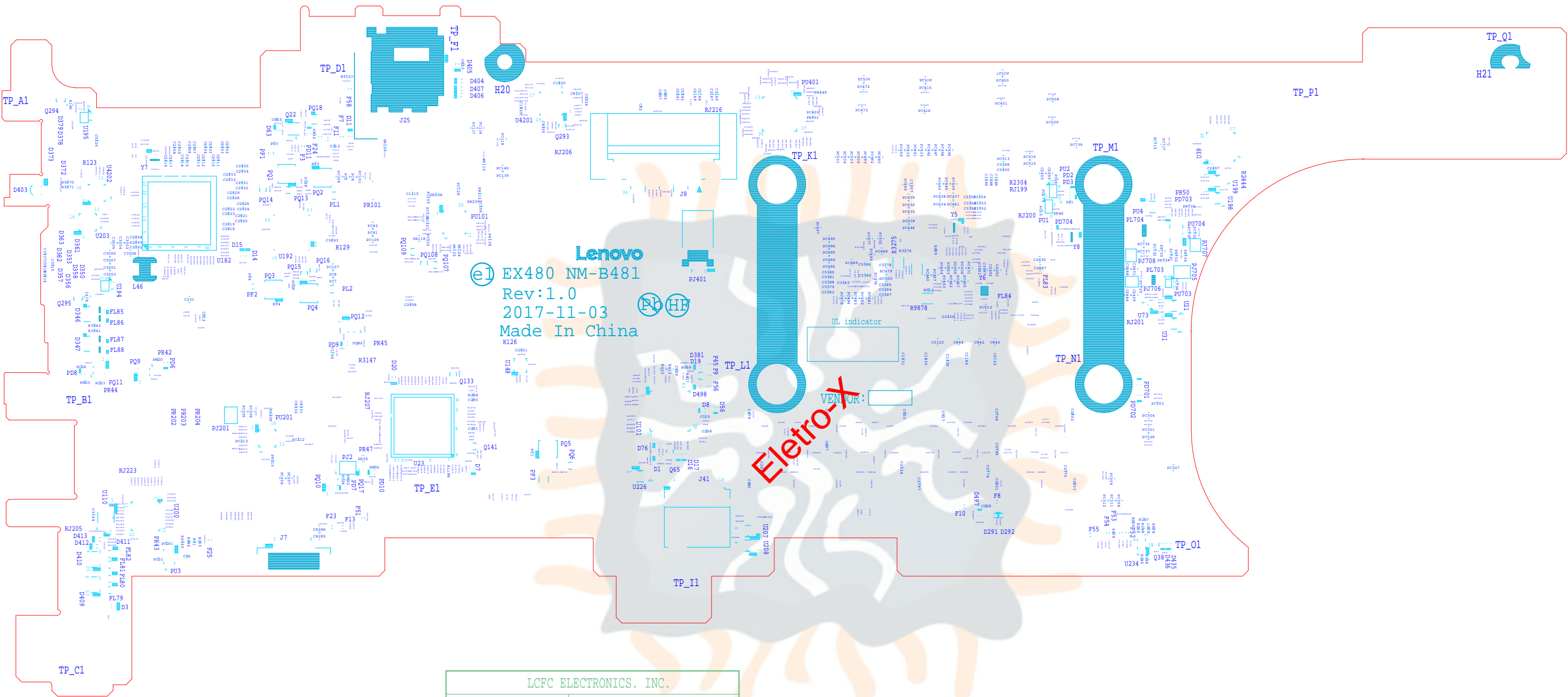
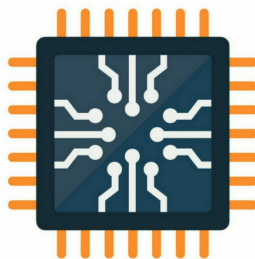


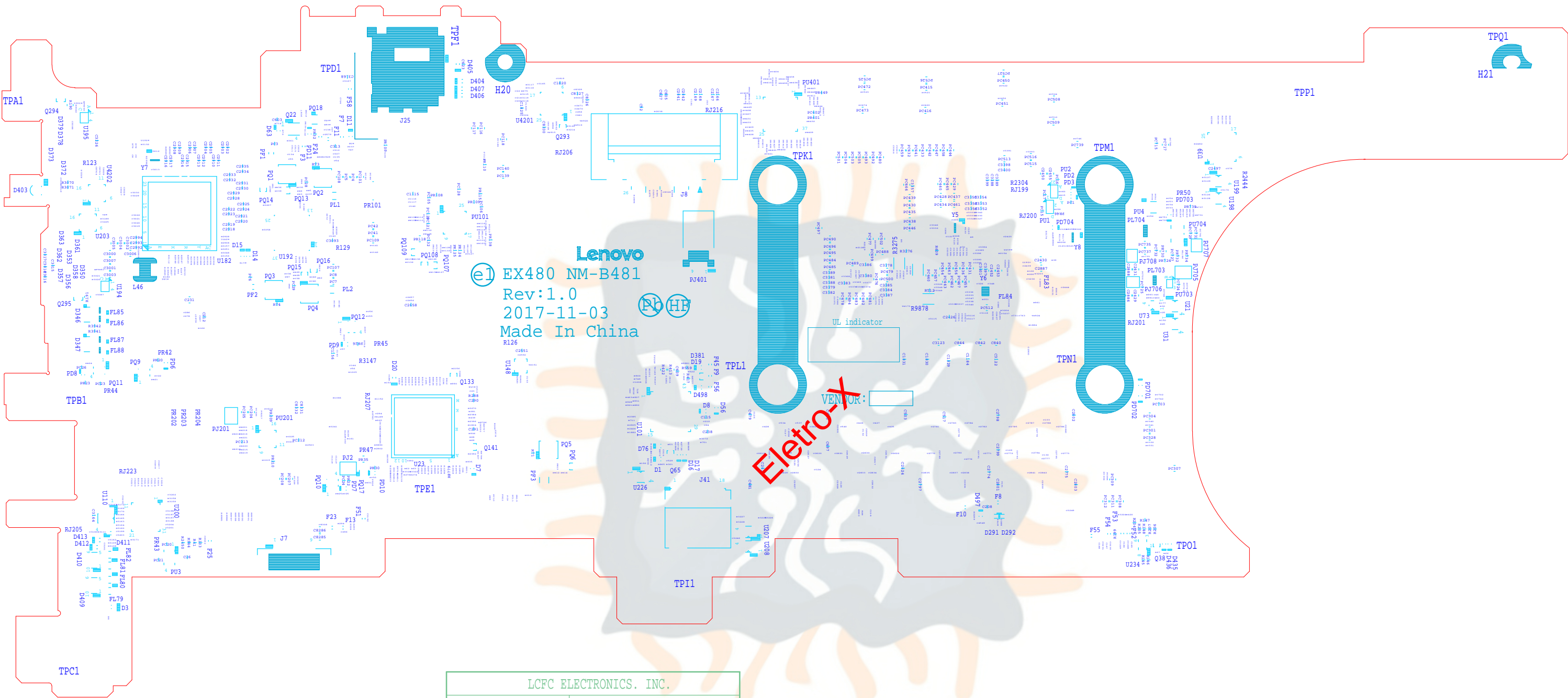
Yoda-2 SVT Logic Schematics

- 1.TITLE PAGE
- 2.EC HISTORY
- 3.CPU(1/16) : DDI/EDP
- 4.CPU(2/16) : DDR CHANNEL-A
- 5.CPU(3/16) : DDR CHANNEL-B
- 6.CPU(4/16) : MISC/JTAG
- 7.CPU(5/16) : LPC/SPI/SMBUS/C-LINK
- 8.CPU(6/16) : LPSS/ISH
- 9.CPU(7/16) : AUDIO/SDXC
- 10.CPU(8/16) : PCIE/USB/SATA
- 11.CPU(9/16) : CSI-2/EMMC
- 12.CPU(10/16) : CLOCK SIGNALS
- 13.CPU(11/16) : SYSTEM PM
- 14.CPU(12/16) : CPU POWER (1/2)
- 15.CPU(13/16) : CPU POWER (2/2)
- 16.CPU(14/16) : PCH POWER
- 17.CPU(15/16) : GND
- 18.CPU(16/16) : CFG/RESERVED
- 19.XDP CONNECOR
- 20.RTC BATTERY
- 21.SPI FLASH
- 22.LPDDR3 Base Memory CH-A(1/2)
- 23.LPDDR3 Base Memory CH-A(2/2)
- 24.LPDDR3 Base Memory CH-B(1/2)
- 25.LPDDR3 Base Memory CH-B(2/2)
- 26.LCD/LID/MIC/CAMER
- 27.PWR SW/AC LED
- 28.BLANK
- 29.HDMI CONNECTOR
- 30.ALPINERIDGE(1/2)
- 31.ALPINERIDGE(1/2)
- 32.THUNDERBOLT PD
- 33.THUNDERBOLT DMUX
- 34.THUNDERBOLT CONNECTOR
- 35.M.2 SOCKET 3 MODULE I/F
- 36.USB POWER/CONN
- 37.BLANK
- 38.GBE JACKSONVILLE
- 39.DOCKING CONNECTOR
- 40.M.2 SOCKET 1 MODULE I/F
- 41.M.2 SOCKET 2 MODULE I/F
- 42.MEDIA CARD CONTROLLER
- 43.MEDIA CARD INTERFACE
- 44.AUDIO SUBC CONNECTOR I/F
- 45.BLANK
- 46.BLANK
- 47.BLANK
- 48.BLANK
- 49.AUDIO BEEP
- 50.BLANK
- 51.MEC1663 (1/3)
- 52.MEC1663 (2/3)
- 53.MEC1663 (3/3)
- 54.KEYBORD/TRACK POINT
- 55.TOUCH PAD/MFC/FPR
- 56.FAN CONNECTER
- 57.APS G-SENSOR
- 58.DISCRETE TPM 2.0
- 59.SMBUS SWITCH/LPC DEBUG PORT
- 60.THINK ENGINE-3(1/2)
- 61.THINK ENGINE-3(2/2)
- 62.DC-IN
- 63.BLANK
- 64.BATTEY
- 65.BATTEY CHARGER(BQ25700A)
- 66.DC/DC VCC5M/VCC3M(TPS51285B-1)
- 67.DC/DC IMVP8 CONTROLLER(MP2949A)
- 68.DC/DC VCCCPUCORE(MP86901C)
- 69.DC/DC VCCGFXCORE_I(MP86901C)
- 70.DC/DC VCCSA(MP86901A)
- 71.BLANK
- 72.DC/DC VCCCPUIO(NB695)
- 73.DC/DC VCC1R0_SUS(NB694)
- 74.LOAD SW VCCST&VCCSTG
- 75.DC/DC VCC1R2A(NB687)
- 76.BLANK
- 77.DD/DC VCC1R8 A(BU90104GWZ)
- 78.DD/DC VCC1R8_SUS(BU90104GWZ)
- 79.DD/DC VCCPCHCORE(NB695)
- 80.BLANK
- 81.BLANK
- 82.LOAD SW PCH SUS/MS
- 83.LOAD SW LAN/VCC1R2B_HDMI
- 84.LOAD SW B
- 85.LOAD SW WLAN
- 86.PTH FOR SCREW HOLES

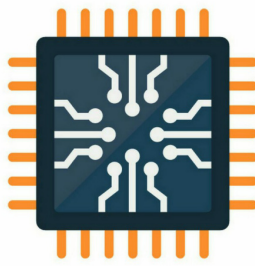


LCFC ELECTRONICS. INC.	
PROJECT	EX480
BOARD NO.	NM-B481 REV:1.0
LAYER NO.	
DRAW BY	Ruby.Chen
Issued Date	2017-11-03
Security level	Confidential
Decipherment date	2018-11-03





LCFC ELECTRONICS. INC.	
PROJECT	EX480
BOARD NO.	NM-B481 REV:1.0
LAYER NO.	
DRAW BY	Ruby.Chen
Issued Date	2017-11-03
Security level	Confidential
Decipherment date	2018-11-03



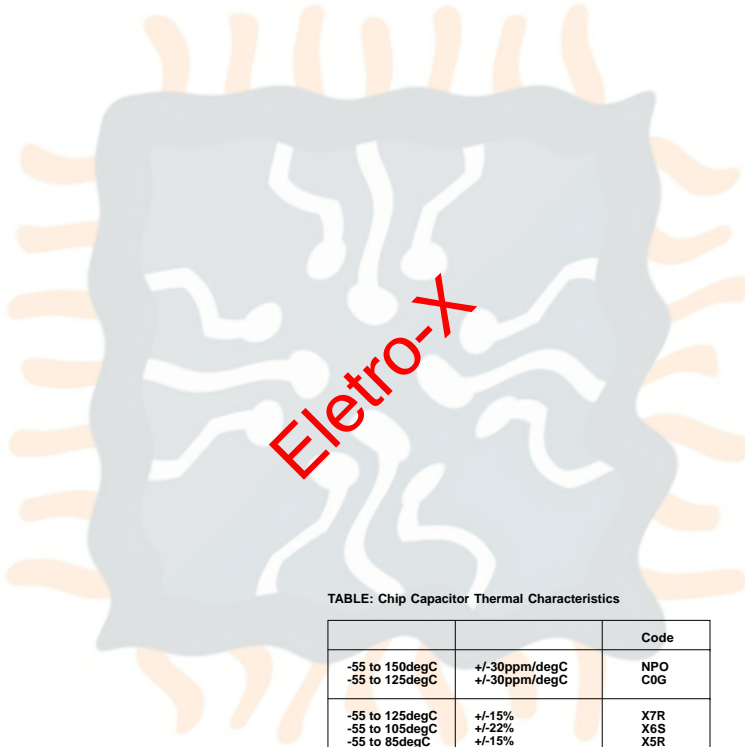


TABLE: Chip Capacitor Thermal Characteristics

		Code
-55 to 150degC	+/-30ppm/degC	NPO
-55 to 125degC	+/-30ppm/degC	C0G
-55 to 125degC	+/-15%	X7R
-55 to 105degC	+/-22%	X6S
-55 to 85degC	+/-15%	X5R

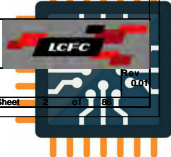
TABLE: Chip Capacitor Tolerance

Tolerance	Code
+/-0.25pF	C
+/-0.5pF	D
+/-5%	J
+/-10%	K
+/-20%	M
+80/-20%	Z

TABLE: Chip Part Dimension

Size [mm]	mm Size Code	Inch Size Code
0.40 x 0.20	0402	01005
0.60 x 0.30	0603	0201
1.00 x 0.50	1005	0402
1.60 x 0.80	1608	0603
2.00 x 1.25	2125	0805
2.00 x 1.60	2016	0806
2.50 x 2.00	2520	1008
3.20 x 1.60	3216	1206
3.20 x 2.50	3225	1210
4.50 x 1.60	4516	1806
4.50 x 2.50	4525	1810
4.50 x 3.20	4532	1812
5.00 x 2.50	5025	2010
6.40 x 3.20	6432	2512

↑
LOGIC



ELETRO-2

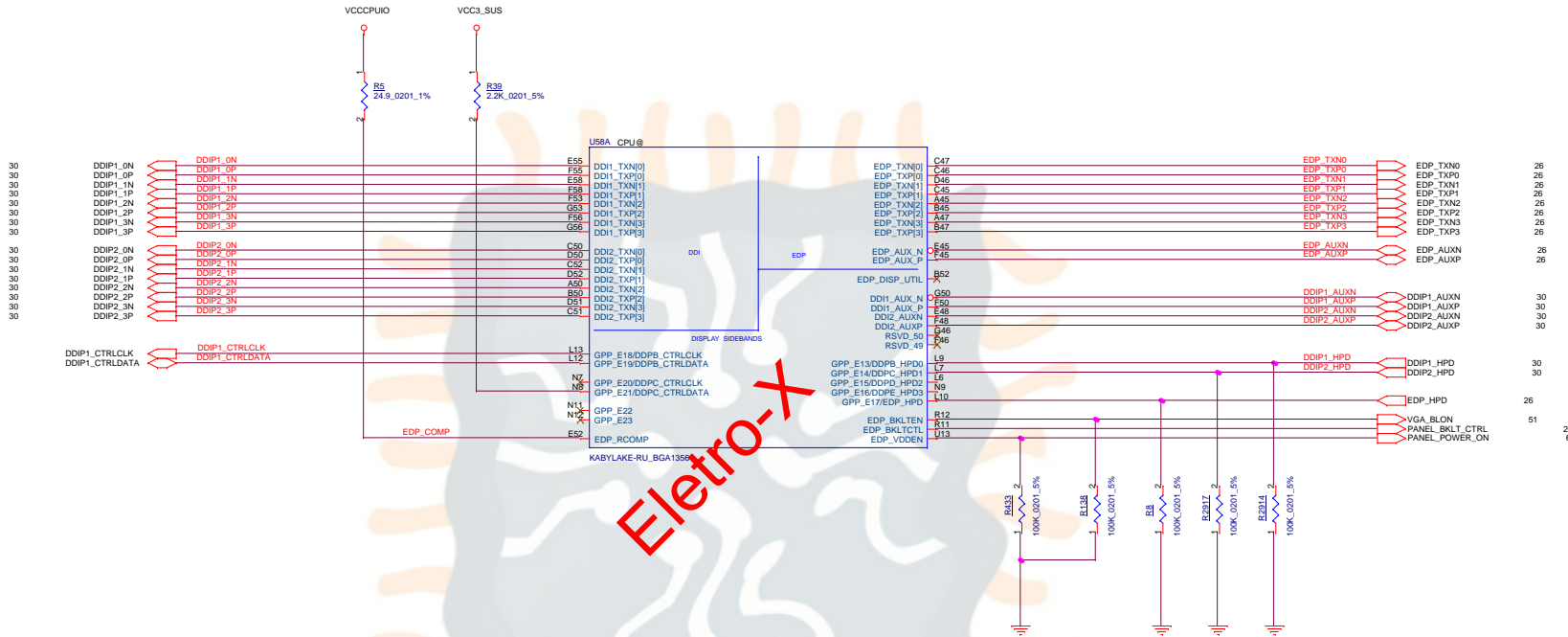
TABLE : Functional Strap

DDPB_CTRLDATA

HIGH	Port B is detected.
LOW	Port B is not detected.

DDPC_CTRLDATA

HIGH	Port C is detected.
LOW	Port C is not detected.



Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			
Size	Document Number	Yoda-2	
Author	Friday, November 03, 2017	1 Sheet	



TABLE

	Pin	Interleave	Non-Interleave
Block 0	AL71	DDR0_DQ[0]	DDR0_DQ[0]
	AL68	DDR0_DQ[1]	DDR0_DQ[1]
	AN68	DDR0_DQ[2]	DDR0_DQ[2]
	AN69	DDR0_DQ[3]	DDR0_DQ[3]
	AL70	DDR0_DQ[4]	DDR0_DQ[4]
	AL69	DDR0_DQ[5]	DDR0_DQ[5]
	AN70	DDR0_DQ[6]	DDR0_DQ[6]
	AN71	DDR0_DQ[7]	DDR0_DQ[7]
	AR70	DDR0_DQ[8]	DDR0_DQ[8]
	AR68	DDR0_DQ[9]	DDR0_DQ[9]
	AU71	DDR0_DQ[10]	DDR0_DQ[10]
	AU68	DDR0_DQ[11]	DDR0_DQ[11]
	AR71	DDR0_DQ[12]	DDR0_DQ[12]
	AR69	DDR0_DQ[13]	DDR0_DQ[13]
	AU70	DDR0_DQ[14]	DDR0_DQ[14]
	AU69	DDR0_DQ[15]	DDR0_DQ[15]
Block 2	BB65	DDR0_DQ[16]	DDR0_DQ[32]
	AW65	DDR0_DQ[17]	DDR0_DQ[33]
	AW63	DDR0_DQ[18]	DDR0_DQ[34]
	AY63	DDR0_DQ[19]	DDR0_DQ[35]
	BA65	DDR0_DQ[20]	DDR0_DQ[36]
	AY65	DDR0_DQ[21]	DDR0_DQ[37]
	BA63	DDR0_DQ[22]	DDR0_DQ[38]
	BB63	DDR0_DQ[23]	DDR0_DQ[39]
	BA61	DDR0_DQ[24]	DDR0_DQ[40]
	BB59	DDR0_DQ[25]	DDR0_DQ[41]
	AW59	DDR0_DQ[26]	DDR0_DQ[42]
	BB59	DDR0_DQ[27]	DDR0_DQ[43]
	AY61	DDR0_DQ[28]	DDR0_DQ[44]
	BA59	DDR0_DQ[29]	DDR0_DQ[45]
	AY59	DDR0_DQ[30]	DDR0_DQ[46]
		DDR0_DQ[31]	DDR0_DQ[47]
Block 4	AY39	DDR0_DQ[32]	DDR1_DQ[0]
	AW39	DDR0_DQ[33]	DDR1_DQ[1]
	AY37	DDR0_DQ[34]	DDR1_DQ[2]
	AW37	DDR0_DQ[35]	DDR1_DQ[3]
	BB39	DDR0_DQ[36]	DDR1_DQ[4]
	BA37	DDR0_DQ[37]	DDR1_DQ[5]
	BB37	DDR0_DQ[38]	DDR1_DQ[6]
	AY35	DDR0_DQ[39]	DDR1_DQ[7]
	AW35	DDR0_DQ[40]	DDR1_DQ[8]
	AY33	DDR0_DQ[41]	DDR1_DQ[9]
	AW33	DDR0_DQ[42]	DDR1_DQ[10]
	BB35	DDR0_DQ[43]	DDR1_DQ[11]
	BB35	DDR0_DQ[44]	DDR1_DQ[12]
	BA35	DDR0_DQ[45]	DDR1_DQ[13]
	BB33	DDR0_DQ[46]	DDR1_DQ[14]
		DDR0_DQ[47]	DDR1_DQ[15]
Block 6	AY31	DDR0_DQ[48]	DDR1_DQ[32]
	AW31	DDR0_DQ[49]	DDR1_DQ[33]
	AY29	DDR0_DQ[50]	DDR1_DQ[34]
	AW29	DDR0_DQ[51]	DDR1_DQ[35]
	BB31	DDR0_DQ[52]	DDR1_DQ[36]
	BA31	DDR0_DQ[53]	DDR1_DQ[37]
	BA29	DDR0_DQ[54]	DDR1_DQ[38]
	BB29	DDR0_DQ[55]	DDR1_DQ[39]
	AY27	DDR0_DQ[56]	DDR1_DQ[40]
	AW27	DDR0_DQ[57]	DDR1_DQ[41]
	AY25	DDR0_DQ[58]	DDR1_DQ[42]
	AW25	DDR0_DQ[59]	DDR1_DQ[43]
	BB27	DDR0_DQ[60]	DDR1_DQ[44]
	BA27	DDR0_DQ[61]	DDR1_DQ[45]
	BA25	DDR0_DQ[62]	DDR1_DQ[46]
	BB25	DDR0_DQ[63]	DDR1_DQ[47]

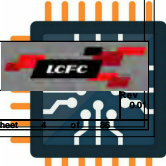
LOGIC

TABLE

	Pin	Interleave	Non-Interleave
Block 0	AM70	DDR0_DQSN[0]	DDR0_DQSN[0]
	AM69	DDR0_DQSP[0]	DDR0_DQSP[0]
	AT69	DDR0_DQSN[1]	DDR0_DQSN[1]
	AT70	DDR0_DQSP[1]	DDR0_DQSP[1]
Block 2	BA64	DDR0_DQSN[2]	DDR0_DQSN[4]
	AY64	DDR0_DQSP[2]	DDR0_DQSP[4]
	AY60	DDR0_DQSN[3]	DDR0_DQSN[5]
	BA60	DDR0_DQSP[3]	DDR0_DQSP[5]
Block 4	BA38	DDR0_DQSN[4]	DDR1_DQSN[0]
	AY38	DDR0_DQSP[4]	DDR1_DQSP[0]
	AY34	DDR0_DQSN[5]	DDR1_DQSN[1]
	BA34	DDR0_DQSP[5]	DDR1_DQSP[1]
Block 6	BA30	DDR0_DQSN[6]	DDR1_DQSN[4]
	AY30	DDR0_DQSP[6]	DDR1_DQSP[4]
	AY26	DDR0_DQSN[7]	DDR1_DQSN[5]
	BA26	DDR0_DQSP[7]	DDR1_DQSP[5]

LOGIC

Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
This sheet of engineering drawing is the proprietary property of LC Future Center, and contains confidential and trade secret information. This sheet may not be transferred from the custody of the competent division of R&D Department except as authorized by LC Future Center. Neither this sheet nor the information it contains may be used by or disclosed to any third party without prior written consent of LC Future Center.			Size Document Number Cuepin Yoda-2 Date: Friday, November 03, 2017



TABLE

	Pin	Interleave	Non-Interleave
Block 1	AF65	DDR1_DQ[0]	DDR0_DQ[16]
	AF64	DDR1_DQ[1]	DDR0_DQ[17]
	AK65	DDR1_DQ[2]	DDR0_DQ[18]
	AK64	DDR1_DQ[3]	DDR0_DQ[19]
	AF66	DDR1_DQ[4]	DDR0_DQ[20]
	AF67	DDR1_DQ[5]	DDR0_DQ[21]
	AK67	DDR1_DQ[6]	DDR0_DQ[22]
	AK66	DDR1_DQ[7]	DDR0_DQ[23]
	AF70	DDR1_DQ[8]	DDR0_DQ[24]
	AF68	DDR1_DQ[9]	DDR0_DQ[25]
	AH71	DDR1_DQ[10]	DDR0_DQ[26]
	AH68	DDR1_DQ[11]	DDR0_DQ[27]
	AF71	DDR1_DQ[12]	DDR0_DQ[28]
	AF69	DDR1_DQ[13]	DDR0_DQ[29]
	AH70	DDR1_DQ[14]	DDR0_DQ[30]
	AH69	DDR1_DQ[15]	DDR0_DQ[31]
Block 3	AT66	DDR1_DQ[16]	DDR0_DQ[48]
	AU66	DDR1_DQ[17]	DDR0_DQ[49]
	AP65	DDR1_DQ[18]	DDR0_DQ[50]
	AN65	DDR1_DQ[19]	DDR0_DQ[51]
	AN66	DDR1_DQ[20]	DDR0_DQ[52]
	AP66	DDR1_DQ[21]	DDR0_DQ[53]
	AT65	DDR1_DQ[22]	DDR0_DQ[54]
	AU65	DDR1_DQ[23]	DDR0_DQ[55]
	AT61	DDR1_DQ[24]	DDR0_DQ[56]
	AU60	DDR1_DQ[25]	DDR0_DQ[57]
	AN60	DDR1_DQ[26]	DDR0_DQ[58]
	AN61	DDR1_DQ[27]	DDR0_DQ[59]
	AP61	DDR1_DQ[28]	DDR0_DQ[60]
	AT60	DDR1_DQ[29]	DDR0_DQ[61]
	AU60	DDR1_DQ[30]	DDR0_DQ[62]
		DDR1_DQ[31]	DDR0_DQ[63]
Block 5	AU40	DDR1_DQ[32]	DDR1_DQ[16]
	AT40	DDR1_DQ[33]	DDR1_DQ[17]
	AT37	DDR1_DQ[34]	DDR1_DQ[18]
	AU37	DDR1_DQ[35]	DDR1_DQ[19]
	AP40	DDR1_DQ[36]	DDR1_DQ[20]
	AP37	DDR1_DQ[37]	DDR1_DQ[21]
	AR37	DDR1_DQ[38]	DDR1_DQ[22]
	AT33	DDR1_DQ[39]	DDR1_DQ[23]
	AU33	DDR1_DQ[40]	DDR1_DQ[24]
	AU30	DDR1_DQ[41]	DDR1_DQ[25]
	AT30	DDR1_DQ[42]	DDR1_DQ[26]
	AR33	DDR1_DQ[43]	DDR1_DQ[27]
	AP33	DDR1_DQ[44]	DDR1_DQ[28]
	AR30	DDR1_DQ[45]	DDR1_DQ[29]
	AP30	DDR1_DQ[46]	DDR1_DQ[30]
		DDR1_DQ[47]	DDR1_DQ[31]
Block 7	AU27	DDR1_DQ[48]	DDR1_DQ[48]
	AT27	DDR1_DQ[49]	DDR1_DQ[49]
	AT25	DDR1_DQ[50]	DDR1_DQ[50]
	AU25	DDR1_DQ[51]	DDR1_DQ[51]
	AP27	DDR1_DQ[52]	DDR1_DQ[52]
	AN27	DDR1_DQ[53]	DDR1_DQ[53]
	AN25	DDR1_DQ[54]	DDR1_DQ[54]
	AP25	DDR1_DQ[55]	DDR1_DQ[55]
	AT22	DDR1_DQ[56]	DDR1_DQ[56]
	AU21	DDR1_DQ[57]	DDR1_DQ[57]
	AT21	DDR1_DQ[58]	DDR1_DQ[58]
	AN22	DDR1_DQ[59]	DDR1_DQ[59]
	AP22	DDR1_DQ[60]	DDR1_DQ[60]
	AP21	DDR1_DQ[61]	DDR1_DQ[61]
	AN21	DDR1_DQ[62]	DDR1_DQ[62]
		DDR1_DQ[63]	DDR1_DQ[63]

LOGIC

TABLE

	Pin	Interleave	Non-Interleave
Block 1	AH66	DDR1_DQSN[0]	DDR0_DQSN[2]
	AH65	DDR1_DQSP[0]	DDR0_DQSP[2]
	AG69	DDR1_DQSN[1]	DDR0_DQSN[3]
	AG70	DDR1_DQSP[1]	DDR0_DQSP[3]
Block 3	AR66	DDR1_DQSN[2]	DDR0_DQSN[6]
	AR65	DDR1_DQSP[2]	DDR0_DQSP[6]
	AR61	DDR1_DQSN[3]	DDR0_DQSN[7]
	AR60	DDR1_DQSP[3]	DDR0_DQSP[7]
Block 5	AT38	DDR1_DQSN[4]	DDR1_DQSN[2]
	AR38	DDR1_DQSP[4]	DDR1_DQSP[2]
	AT32	DDR1_DQSN[5]	DDR1_DQSN[3]
	AR32	DDR1_DQSP[5]	DDR1_DQSP[3]
Block 7	AR25	DDR1_DQSN[6]	DDR1_DQSN[6]
	AR27	DDR1_DQSP[6]	DDR1_DQSP[6]
	AR22	DDR1_DQSN[7]	DDR1_DQSN[7]
	AR21	DDR1_DQSP[7]	DDR1_DQSP[7]

LOGIC

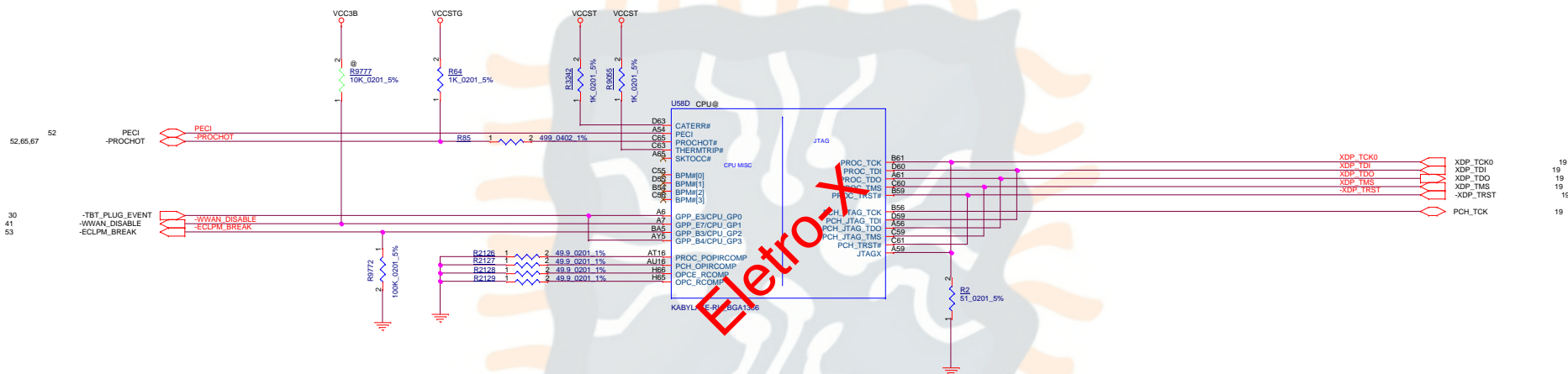
TABLE

Pin	DDR3L	LPDDR3	DDR4
AY48	DDR1_MA[5]	DDR1_CAA[0]	DDR1_MA[5]
AP50	DDR1_MA[9]	DDR1_CAA[1]	DDR1_MA[9]
BA48	DDR1_MA[6]	DDR1_CAA[2]	DDR1_MA[6]
BB48	DDR1_MA[7]	DDR1_CAA[3]	DDR1_MA[8]
AP48	DDR1_MA[7]	DDR1_CAA[4]	DDR1_MA[7]
AP52	DDR1_BA[2]	DDR1_CAA[5]	DDR1_BG[0]
AN50	DDR1_MA[12]	DDR1_CAA[6]	DDR1_MA[12]
AN48	DDR1_MA[11]	DDR1_CAA[7]	DDR1_MA[11]
AN53	DDR1_MA[15]	DDR1_CAA[8]	DDR1_ACT#
AN52	DDR1_MA[14]	DDR1_CAA[9]	DDR1_BG[1]
BA43	DDR1_MA[13]	DDR1_CAB[0]	DDR1_MA[13]
AY43	DDR1_CAS#	DDR1_CAB[1]	DDR1_MA[15]
AY44	DDR1_WE#	DDR1_CAB[2]	DDR1_MA[14]
AW44	DDR1_RAS#	DDR1_CAB[3]	DDR1_MA[16]
BB44	DDR1_BA[0]	DDR1_CAB[4]	DDR1_BA[0]
BA44	DDR1_MA[2]	DDR1_CAB[5]	DDR1_MA[2]
AW46	DDR1_BA[1]	DDR1_CAB[6]	DDR1_BA[1]
AY46	DDR1_MA[10]	DDR1_CAB[7]	DDR1_MA[10]
BA46	DDR1_MA[11]	DDR1_CAB[8]	DDR1_MA[11]
BB46	DDR1_MA[0]	DDR1_CAB[9]	DDR1_MA[0]
BA47	DDR1_MA[3]	Not Used	DDR1_MA[3]
	DDR1_MA[4]	Not Used	DDR1_MA[4]

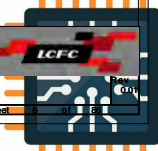
LOGIC

Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
This sheet of engineering drawing is the proprietary property of LC Future Center, and contains confidential and trade secret information. This sheet may not be transferred from the custody of the competent division of R&D Department except as authorized by LC Future Center. Neither this sheet nor the information it contains may be used by or disclosed to any third party without prior written consent of LC Future Center.			Size Document Number Cuslun Yoda-2 Date: Friday, November 03, 2017





Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	CPU(4/16) : MISC/JTAG	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.				Size	Document Number
				Column	Yoda-2
				Date	Friday, November 03, 2017
				Sheet	1



ELECTRO-2

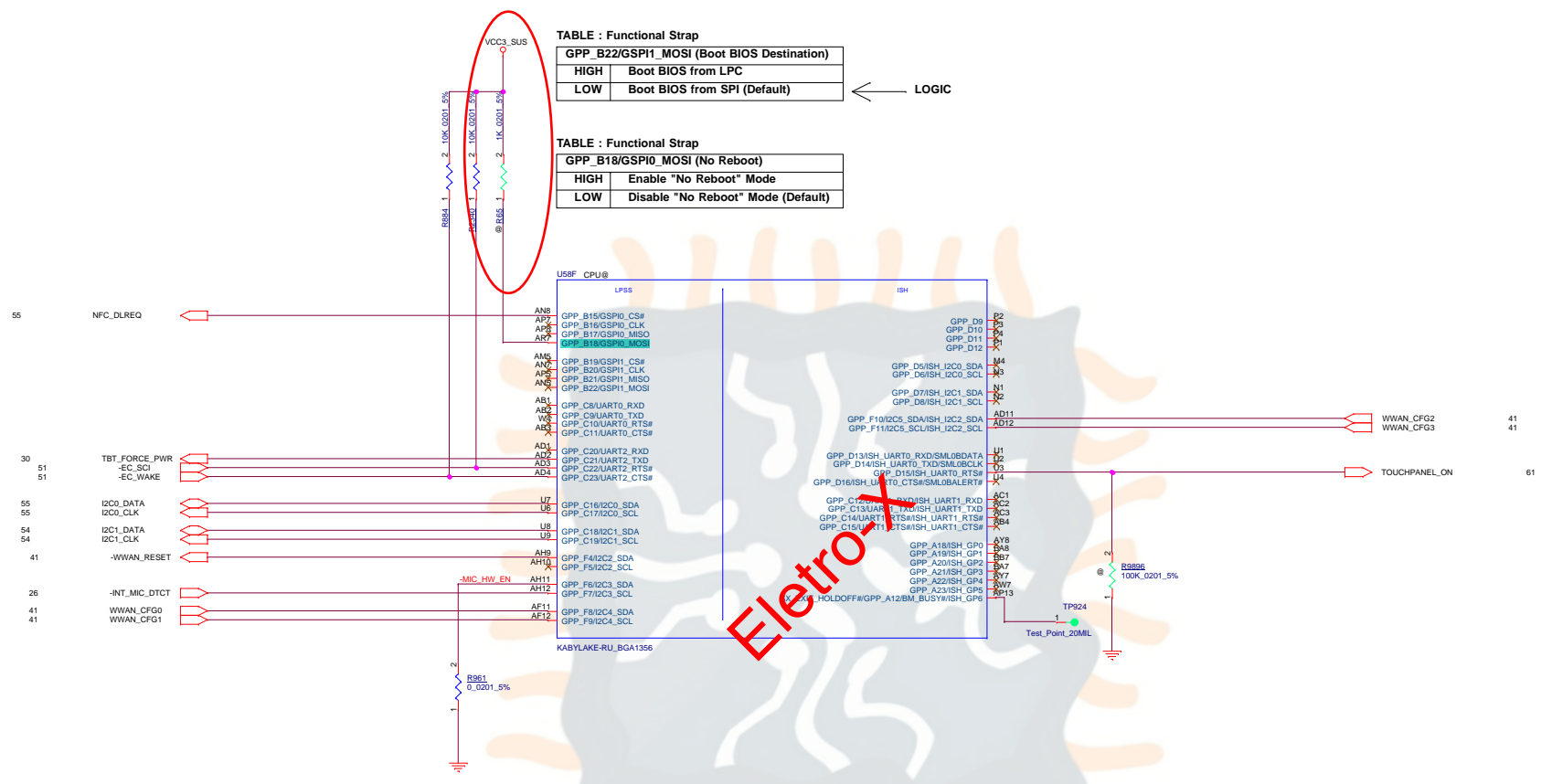


TABLE : Functional Strap

GPP_B22/GSPI1_MOSI (Boot BIOS Destination)	
HIGH	Boot BIOS from LPC
LOW	Boot BIOS from SPI (Default)

← LOGIC

TABLE : Functional Strap

GPP_B18/GSPI0_MOSI (No Reboot)	
HIGH	Enable "No Reboot" Mode
LOW	Disable "No Reboot" Mode (Default)

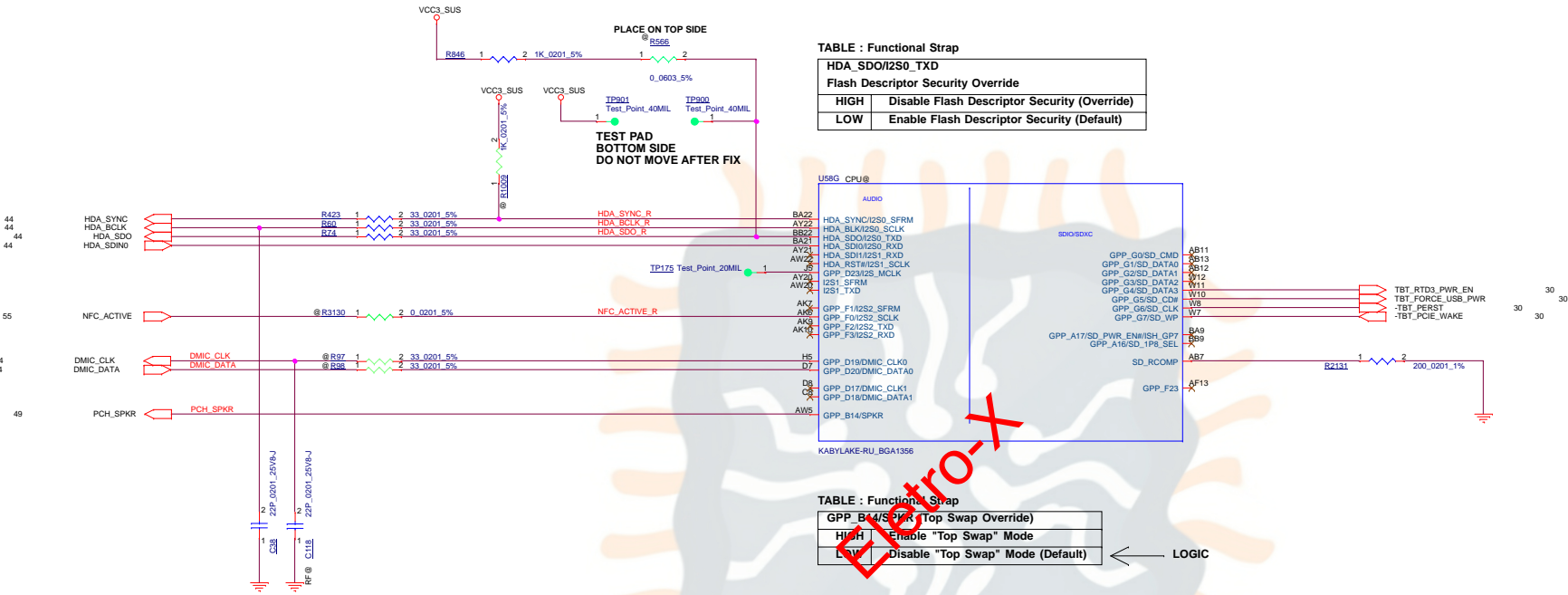


TABLE : Functional Strap

HDA_SDO/I2S0_TXD	
Flash Descriptor Security Override	
HIGH	Disable Flash Descriptor Security (Override)
LOW	Enable Flash Descriptor Security (Default)

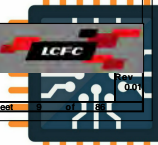
TABLE : Functional Strap

GPP_B14/SPKR (Top Swap Override)	
HIGH	Enable "Top Swap" Mode
LOW	Disable "Top Swap" Mode (Default)

LOGIC

Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		CPU(7/16) : AUDIO/SDXC	
2017/02/17		2014/07/01		Size Document Number	
				Yoda-2	
				Date: Friday, November 03, 2017 Sheet	

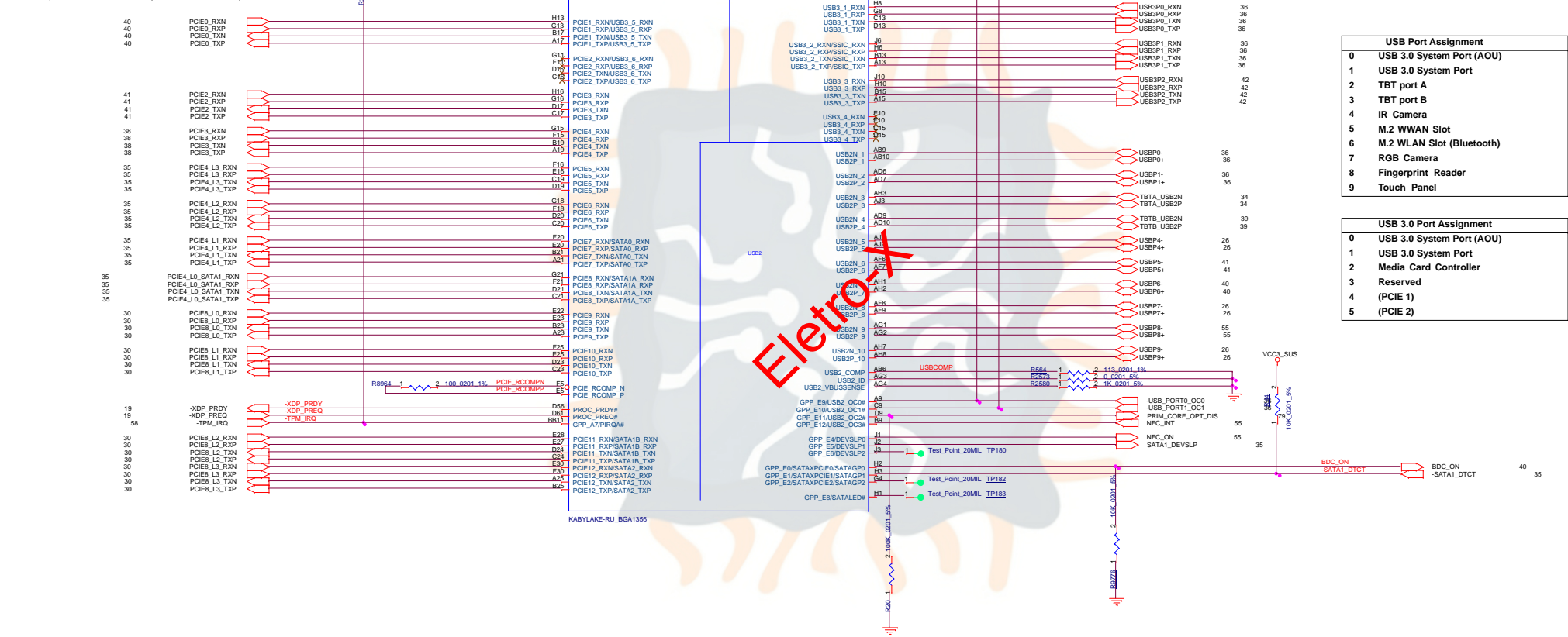
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.



Flexible I/O Configuration			
I/O	High Speed Signals	Configuration	Net Name
Port 1	USB3 1	USB3 1	USB3P0
Port 2	USB3 2/SSIC	USB3 2	USB3P1
Port 3	USB3 3	USB3 3	USB3P2
Port 4	USB3 4	USB3 4	N/A
Port 5	USB3 5/PCIE 1	PCIE 1	PCIE0
Port 6	USB3 6/PCIE 2	PCIE 2	N/A
Port 7	PCIE 3 (GbE)	PCIE 3	PCIE2
Port 8	PCIE 4 (GbE)	PCIE 4 (GbE)	PCIE3
Port 9	PCIE 5 (GbE)	PCIE 5 (x4)	PCIE4_L3
Port 10	PCIE 6	PCIE 6 (x4)	PCIE4_L2
Port 11	PCIE 7/SATA 0	PCIE 7 (x4)	PCIE4_L1
Port 12	PCIE 8/SATA 1A	GPIO STRAP	PCIE4_L0_SATA1
Port 13	PCIE 9 (GbE)	PCIE 9 (x4)	PCIE8_L0
Port 14	PCIE 10 (GbE)	PCIE 10 (x4)	PCIE8_L1
Port 15	PCIE 11/SATA 1B	PCIE 11 (x4)	PCIE8_L2
Port 16	PCIE 12/SATA 2	PCIE 12 (x4)	PCIE8_L3

PCIe Port Assignment	
0	M.2 WLAN Slot Port 0
1	N/A
2	WWAN
3	GbE PHY
4 (x4)	PCIe SSD
8 (x4)	Thunderbolt

SATA Port Assignment	
0	(PCIE 7)
1A	SATA SSD
1B	(PCIE 11)
2	(PCIE 12)



USB Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port
2	TBT port A
3	TBT port B
4	IR Camera
5	M.2 WWAN Slot
6	M.2 WLAN Slot (Bluetooth)
7	RGB Camera
8	Fingerprint Reader
9	Touch Panel

USB 3.0 Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port
2	Media Card Controller
3	Reserved
4	(PCIE 1)
5	(PCIE 2)

TABLE

MEMORYID[4..0]	U170, U171, U172, U173			
00000b 00001b 00010b 00011b	Micron	MT52L512M32D2PF-093WT:B MT52L1G32D4PG-093WT:B	16Gbit DDP 32Gbit QDP	Reserved Reserved 8GB (2133) 16GB (2133)
00100b 00101b 00110b 00111b	Samsung	K4E6E304EB-EGCG K4EBE304EB-EGCG	16Gbit DDP 32Gbit QDP	Reserved Reserved 8GB (2133) 16GB (2133)
01000b 01001b 01010b 01011b	SK Hynix	H9CCNNNBJTALAR-NVD H9CCNNNCLGALAR-NVD	16Gbit DDP 32Gbit QDP	Reserved Reserved 8GB (2133) 16GB (2133)



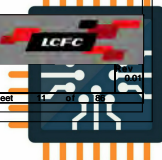
TABLE

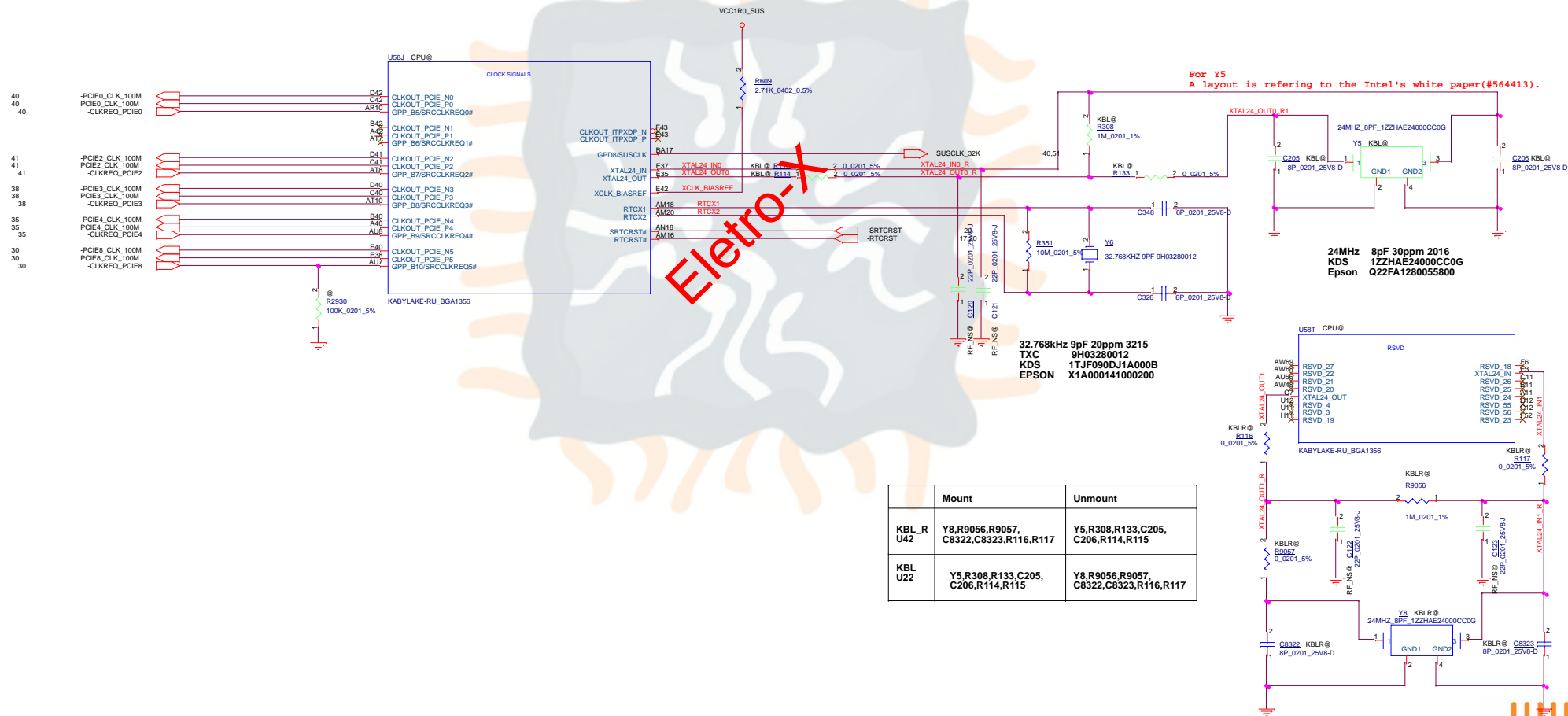
LEVEL	PLANAR ID			
	3	2	1	0
	R43	R47	R113	R48
1	NA	NA	NA	NA
0	ASM	ASM	ASM	ASM

TABLE

LEVEL	PLANARID[3..0]
EVT	0000B
FVT	0001B
SIT	0010B
SIT-R	0011B
SVT/SOVP	0100B

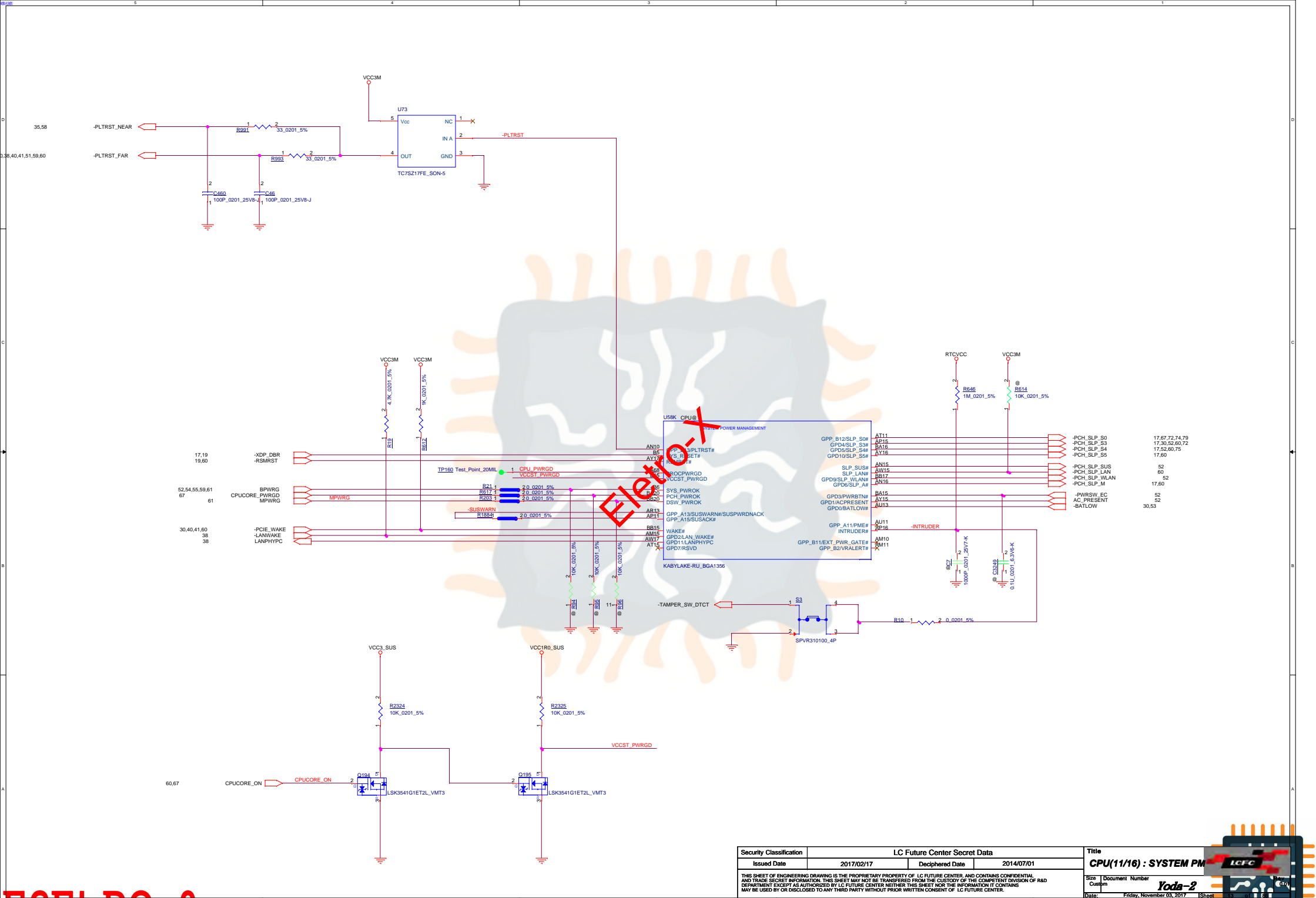
Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			Size Document Number Cust Yoda-2 Date: Friday, November 03, 2017 Sheet

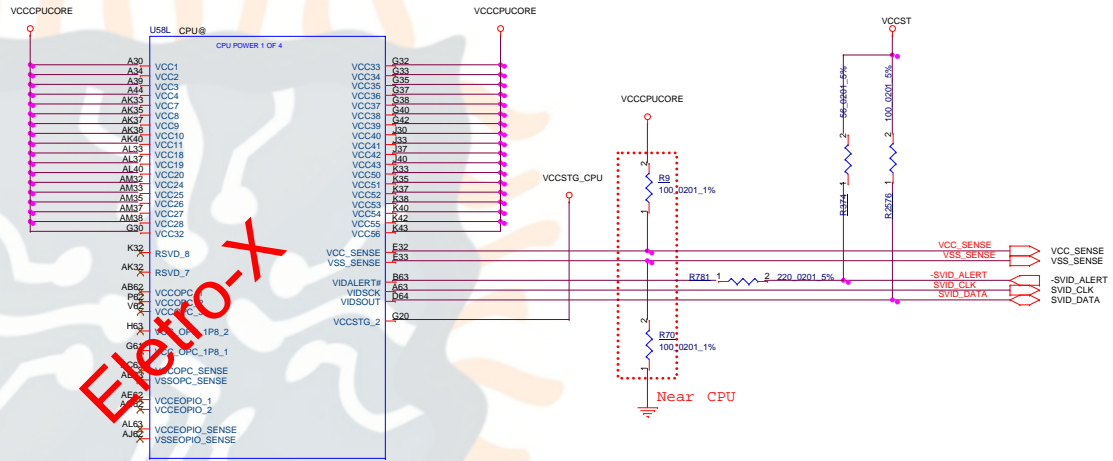
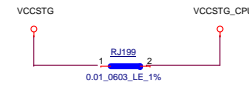
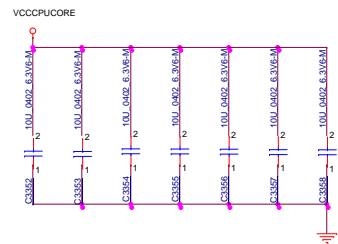
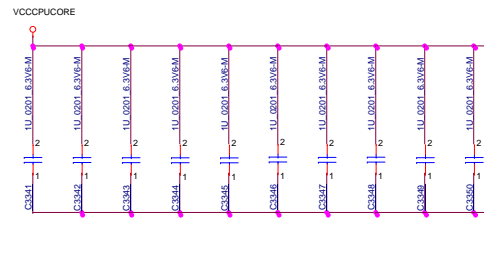
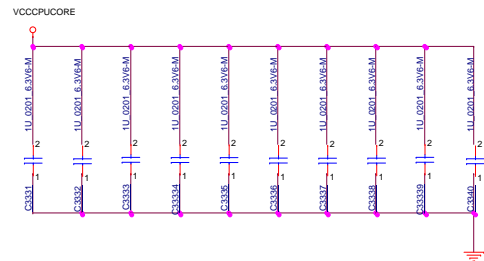
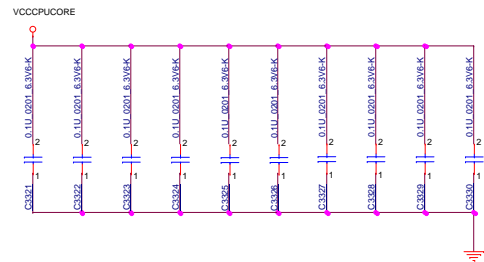




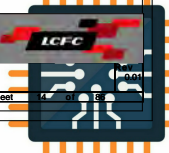
	Mount	Unmount
KBL_R U42	Y8,R9056,R9057, C8322,C8323,R116,R117	Y5,R308,R133,C205, C206,R114,R115
KBL U22	Y5,R308,R133,C205, C206,R114,R115	Y8,R9056,R9057, C8322,C8323,R116,R117

ELECTRO-2





Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			Size Document Number
			Custpm
			Yoda-2
			Date: Friday, November 03, 2017



ELECTRO-X

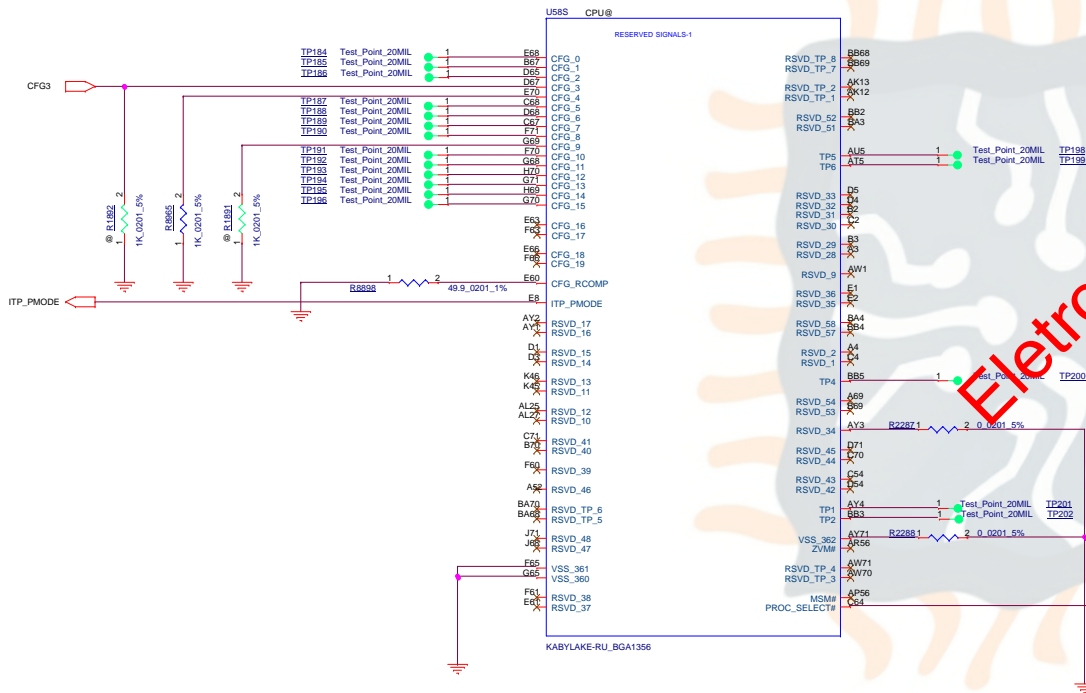
TABLE

CFG0 : Stall Reset Sequence
after PCU PLL Lock until de-asserted
1 : No Stall
0 : Stall

CFG3 : MSR Privacy Bit Feature
1 : MSR (C80h) bit[0] setting
0 : MSR (C80h) bit[0] overridden

CFG4 : eDP Enable
1 : Disabled
0 : Enabled

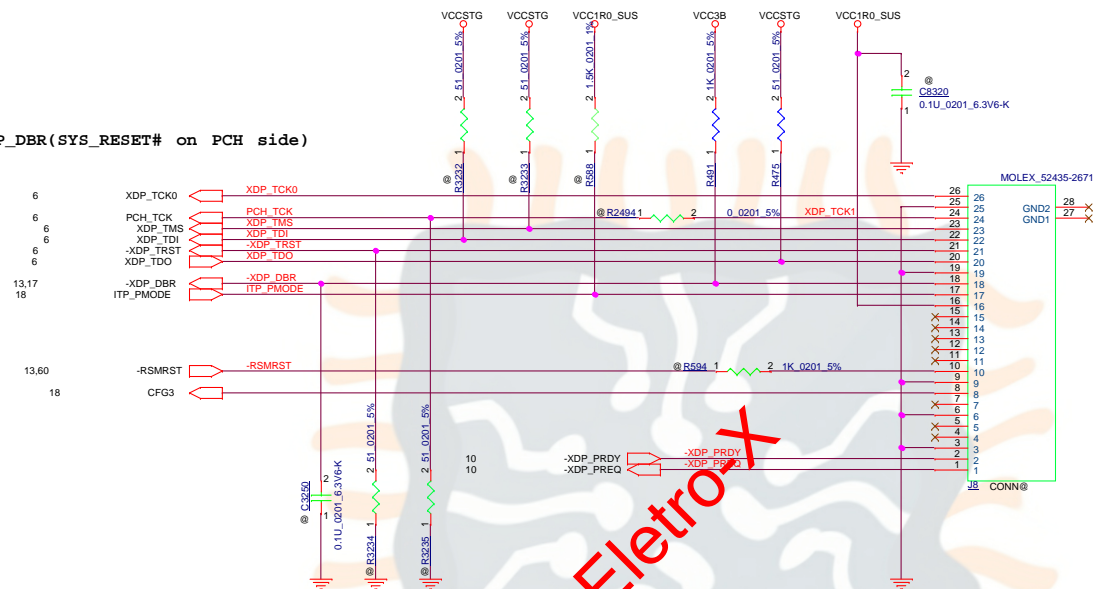
CFG9 : SVID Bus Communication
1 : Enabled
0 : Disabled



Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	CPU(16/16) : CFG/RESERVED	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.				Size	Document Number
				Chuan	Yoda-2
				Date:	Friday, November 03, 2017 [Sheet 1 of 1]



Never remove pull-up on -XDP_DBR(SYS_RESET# on PCH side)

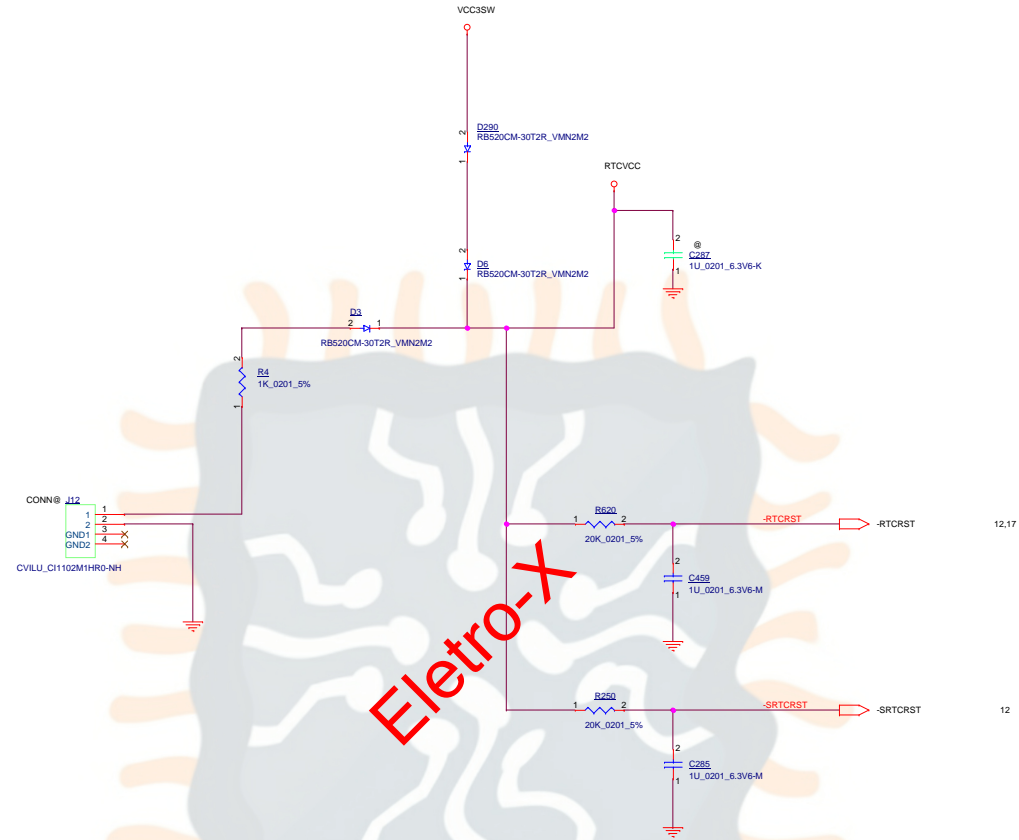


TABLE

Logic	Ref Des	Merged	DCI 2.0
Page 7	R2559	ASM	NO_ASM
Page 18	R1892	ASM	NO_ASM
Page 19	J8	ASM	NO_ASM
	C8320	ASM	NO_ASM
	R475	ASM	ASM
	R491	ASM	ASM
	R588	ASM	NO_ASM
	R594	ASM	NO_ASM
	R2494	ASM	NO_ASM

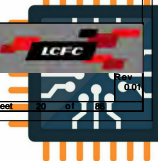
↑
LOGIC

Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			Size Document Number Custom Yoda-2
			Date: Friday, November 03, 2017 Sheet: 1 of 1

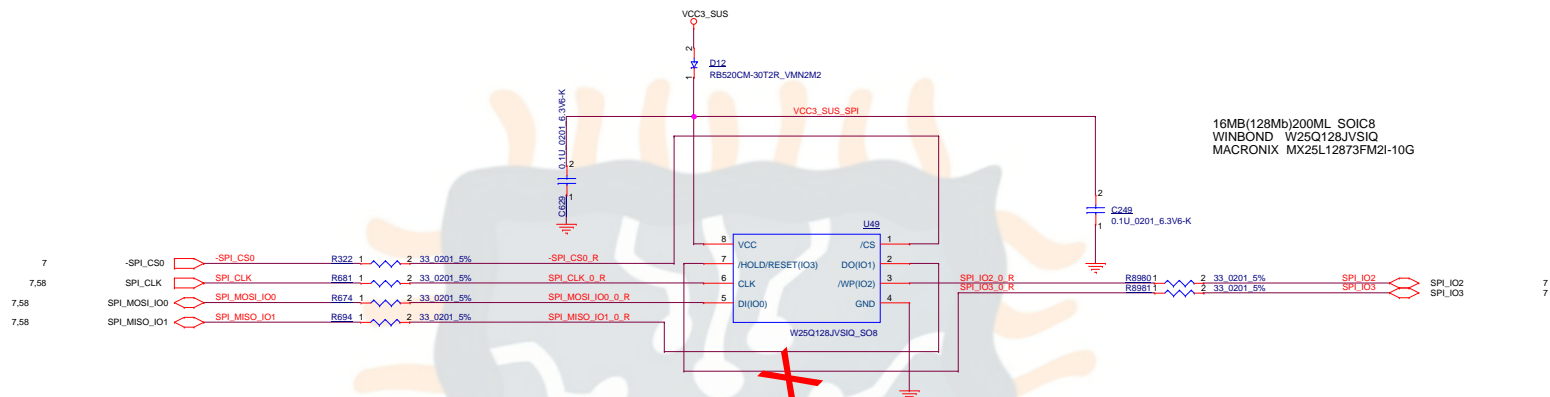


Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			

Size	Document Number	Date
Cuuhm	Yoda-2	Friday, November 03, 2017



ELETR0-2



TABLE

SF100 HEADER INTERFACE (TOP VIEW)					
VCC	D12.1	GND	GND	2	
CS#	R322.2	R681.2	CLK	4	
MOSI	R674.2	R674.2	MOSI	6	
MISO	N/A	N/A	(RESET)	8	

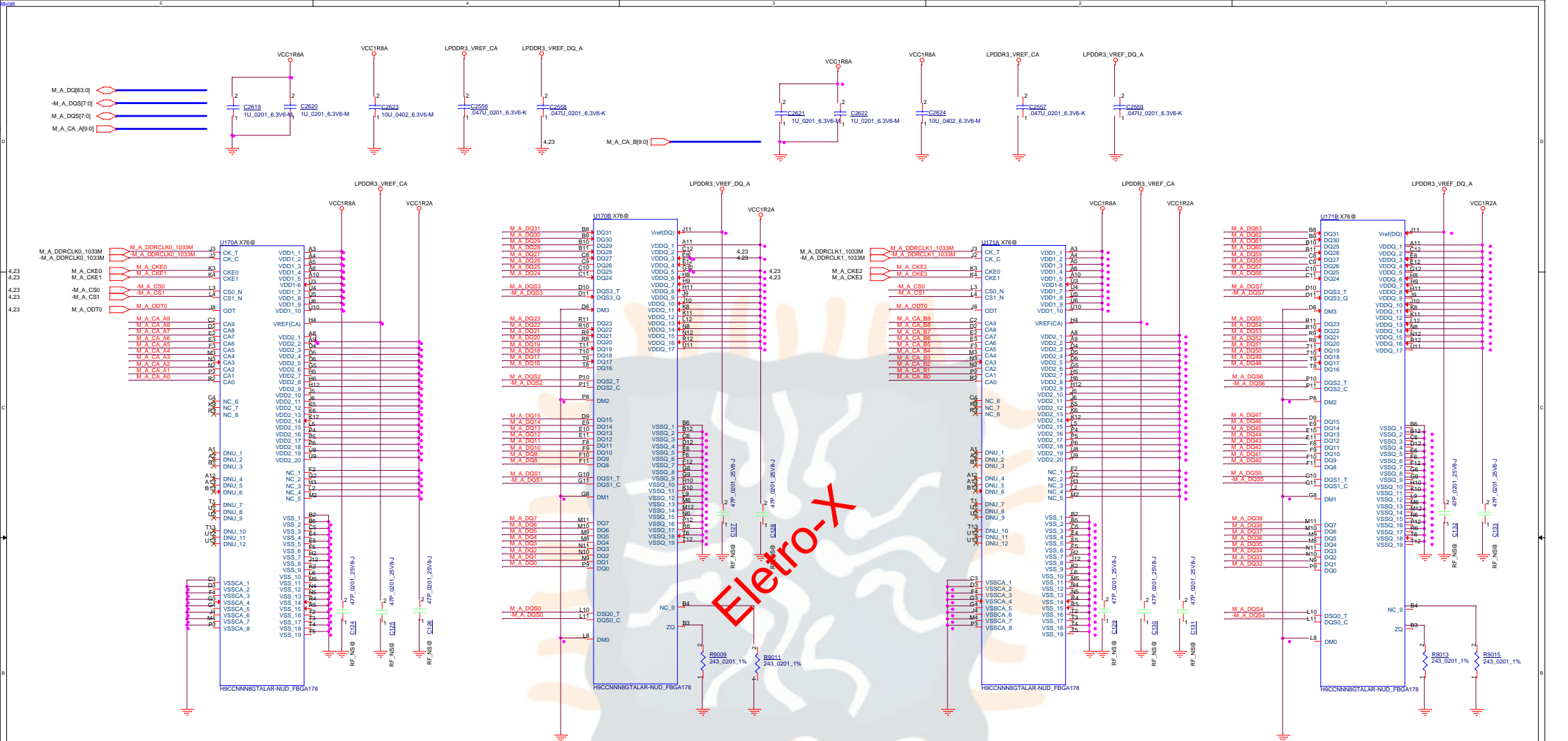
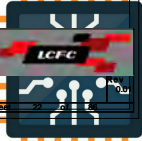


TABLE: LPDDR3 SDRAM Source

Supplier	Capacity	Supplier's P/N	Package Size	Die	Configuration	ZQ0 / ZQ	ZQ1 / NC	CKE0 / CKE	CKE1 / NC	CS0# / CS#	CS1# / NC	Dual Ch	
Micron	16Gb	MT52L512M32D2PF-093 WT-B	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (256Mx32)	ZQ0	NC	CKE0	CKE1	CS0#	CS1#	8GB
	32Gb	MT52L1G32D4PG-093 WT-B	12.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16GB
Samsung	16Gb	K4E6E304EB-EGCG	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (256Mx32)	ZQ	NC	CKE0	CKE1	CS0#	CS1#	8GB
	32Gb	K4E6E304EB-EGCG	11.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16GB
SK hynix	16Gb	H9CCNNNB1TALAR-NVD	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (256Mx32)	ZQ	NC	CKE0	CKE1	CS0#	CS1#	8GB
	32Gb	H9CCNNNCLGALAR-NVD	11.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16GB

Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	LPDDR3 BASE MEMORY CH-A (1/2)	
<p>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.</p>					
Size	Document Number		Yoda-2		
Custm					
Date	Friday, November 03, 2017				



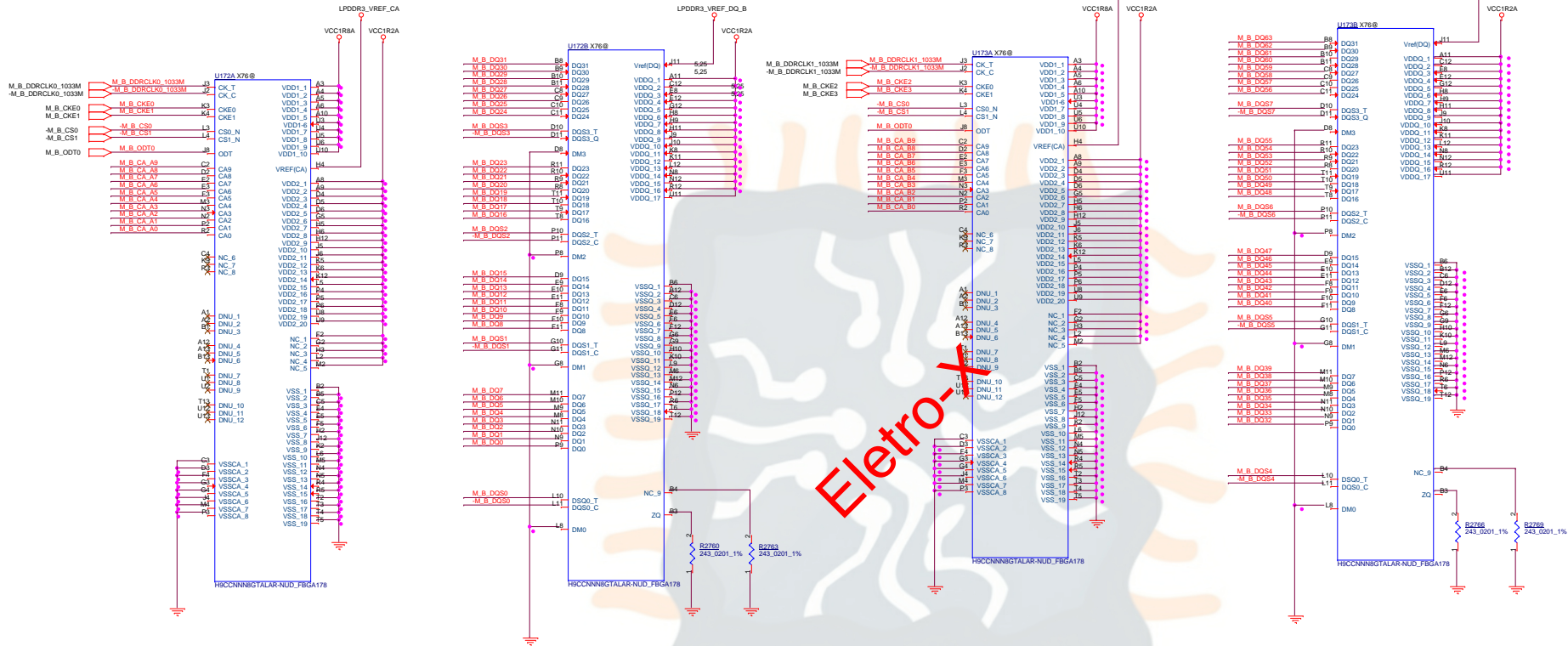
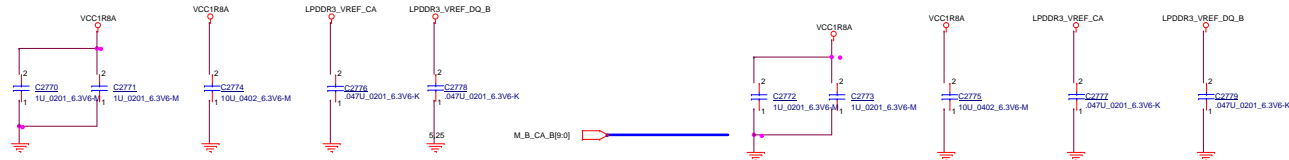
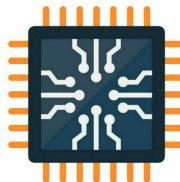
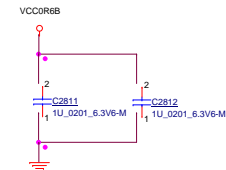
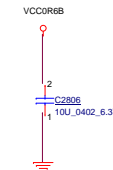
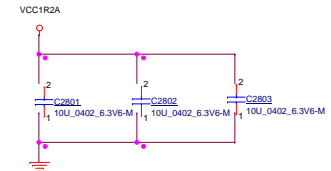
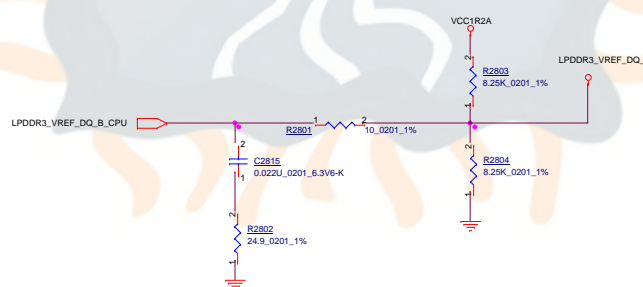
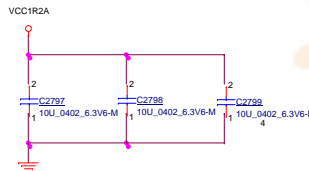
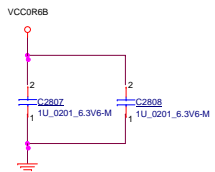
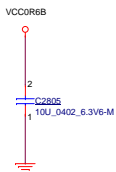
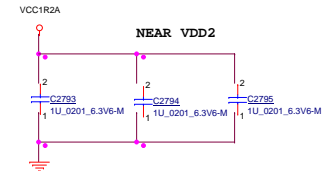
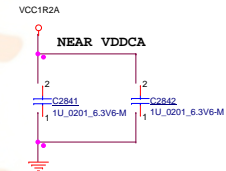
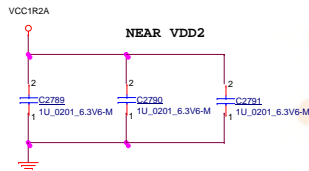
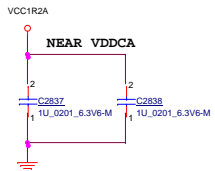
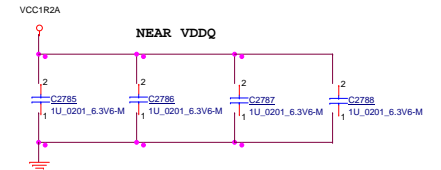
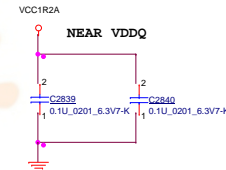
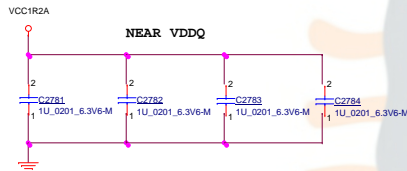
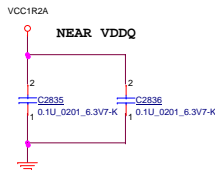
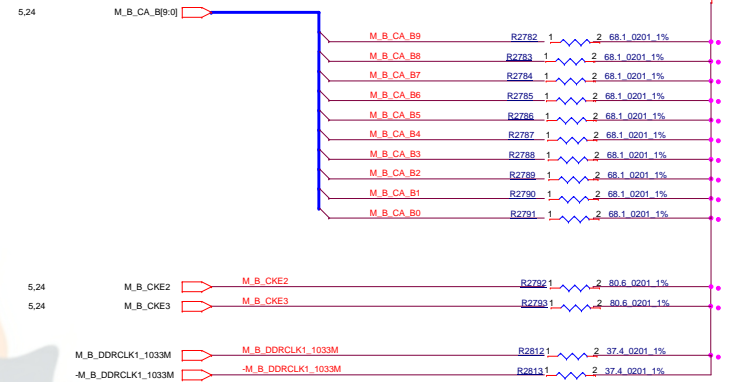
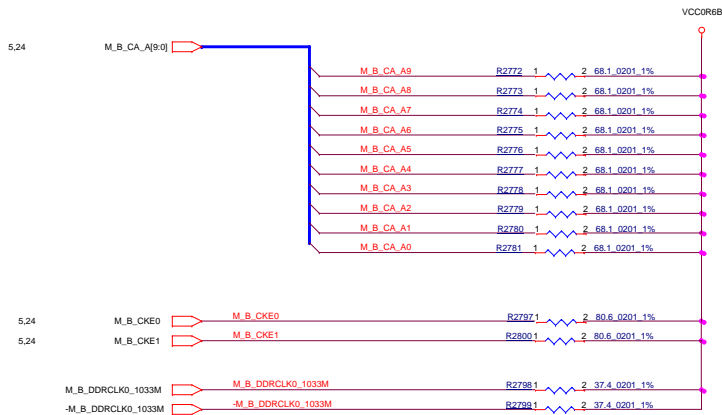


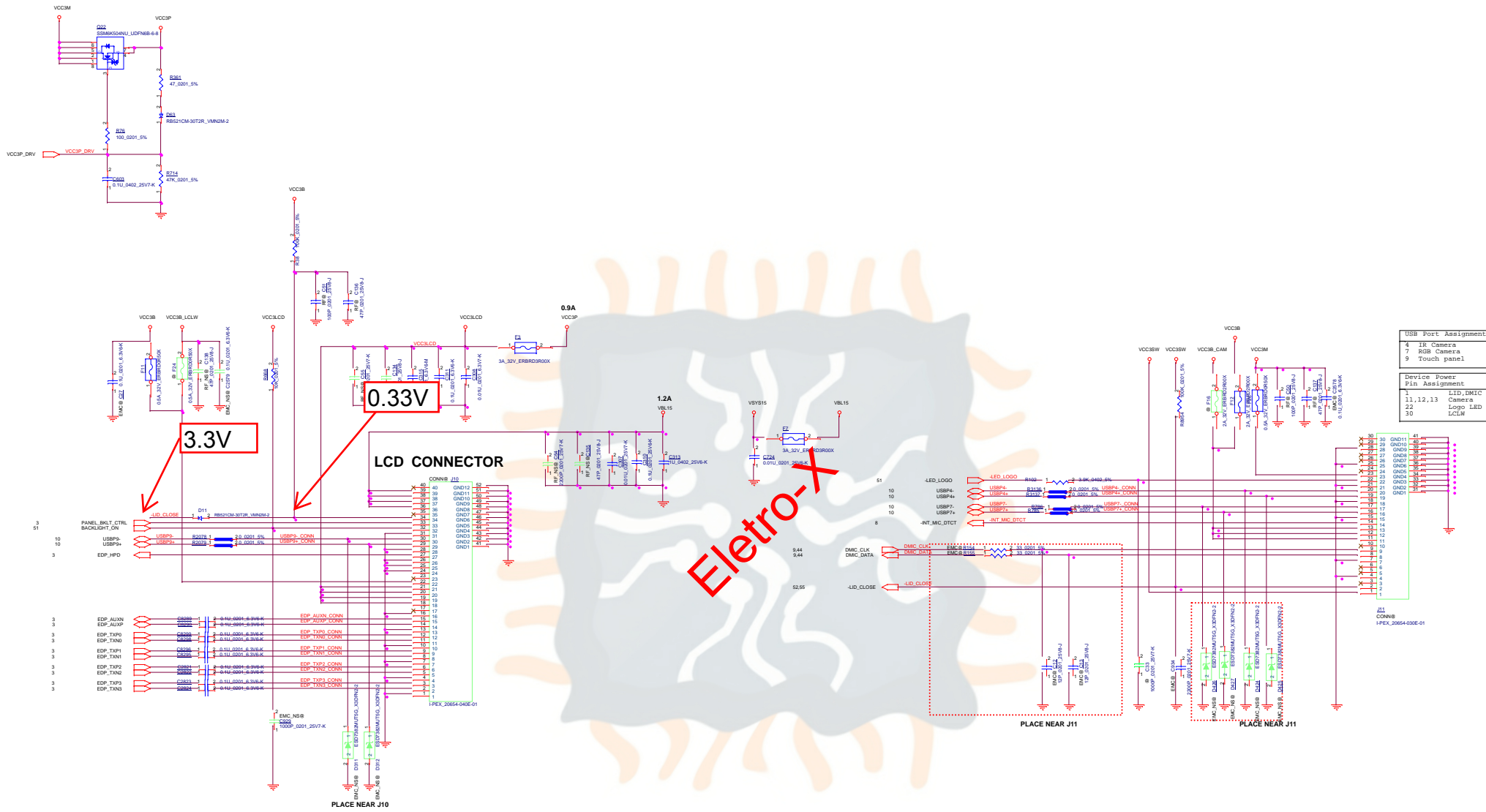
TABLE: LPDDR3 SDRAM Source

Supplier	Capacity	Supplier's P/N	Package Size	Die	Configuration	ZQ0 / ZQ1	NC	CKE0 / CKE1	CKE1 / NC	CS0# / CS#	CS1# / NC	Dual Ch	
Micron	16Gb	MT52L512M32D2PF-093 WT-B	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (512Mx32)	ZQ	NC	CKE0	CKE1	CS0#	CS1#	8Gb
	32Gb	MT52L1G32D4PG-093 WT-B	12.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16Gb
Samsung	16Gb	K4EBE304EB-EGCG	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (512Mx32)	ZQ	NC	CKE0	CKE1	CS0#	CS1#	8Gb
	32Gb	K4EBE304EB-EGCG	11.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16Gb
SK hynix	16Gb	H9CCNNNBJTALAR-NVD	11.0 x 11.5 mm	DDP	8Gb (256Mx32)	2 Rank x (512Mx32)	ZQ	NC	CKE0	CKE1	CS0#	CS1#	8Gb
	32Gb	H9CCNNNCLGALAR-NVD	11.0 x 11.5 mm	QDP	8Gb (512Mx16)	2 Rank x (512Mx32)	ZQ0	ZQ1	CKE0	CKE1	CS0#	CS1#	16Gb

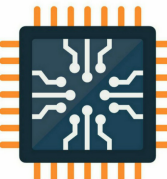
Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	LPDDR3 BASE MEMORY CH-B (1/2)	
<p>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.</p>				Size	Rev
				Custom	0.00
				Yoda-2	
				Date	Friday, November 23, 2017
				Sheet	34 of 36

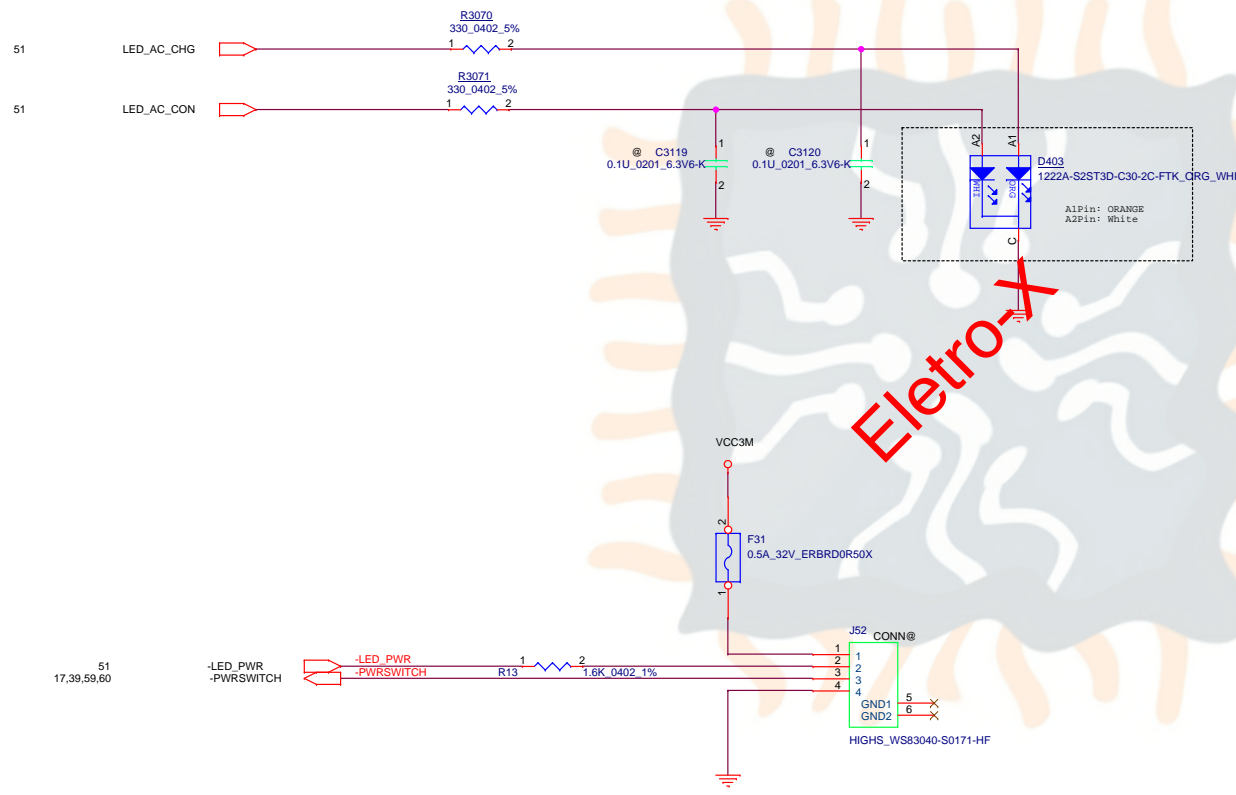






Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Declassified Date	2014/07/01	LC/LID/IMC/CAMERA	
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT THE WRITTEN PERMISSION OF LC FUTURE CENTER. ANY UNAUTHORIZED DISCLOSURE OF THIS INFORMATION IS PROHIBITED.</small>					
Doc. Number	Yoda-3		Rev.	0.01	
Created	2017/02/17	Modified	2017/02/17		





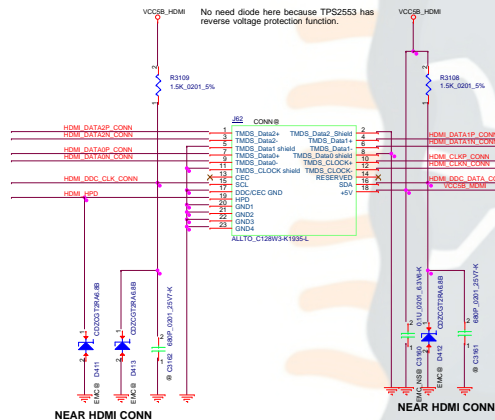
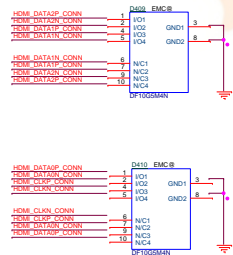
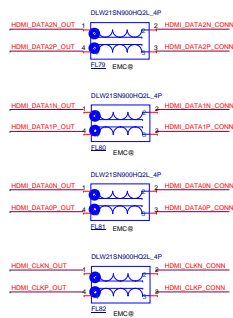
Change from YD-1.
Take distance between VCC3M pin and GND pin.

Security Classification		LC Future Center Secret Data	
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			

Title		PWR SW/AC LED	
Size	Document Number	Yoda-2	
Custom			
Date:	Friday, November 03, 2017	Sheet	27

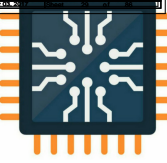
ELETRON-2

TABLE FL79,FL80,FL81,FL82
Murata DLW01SN900HQ2
Chilisin CUW021T-900Y-N

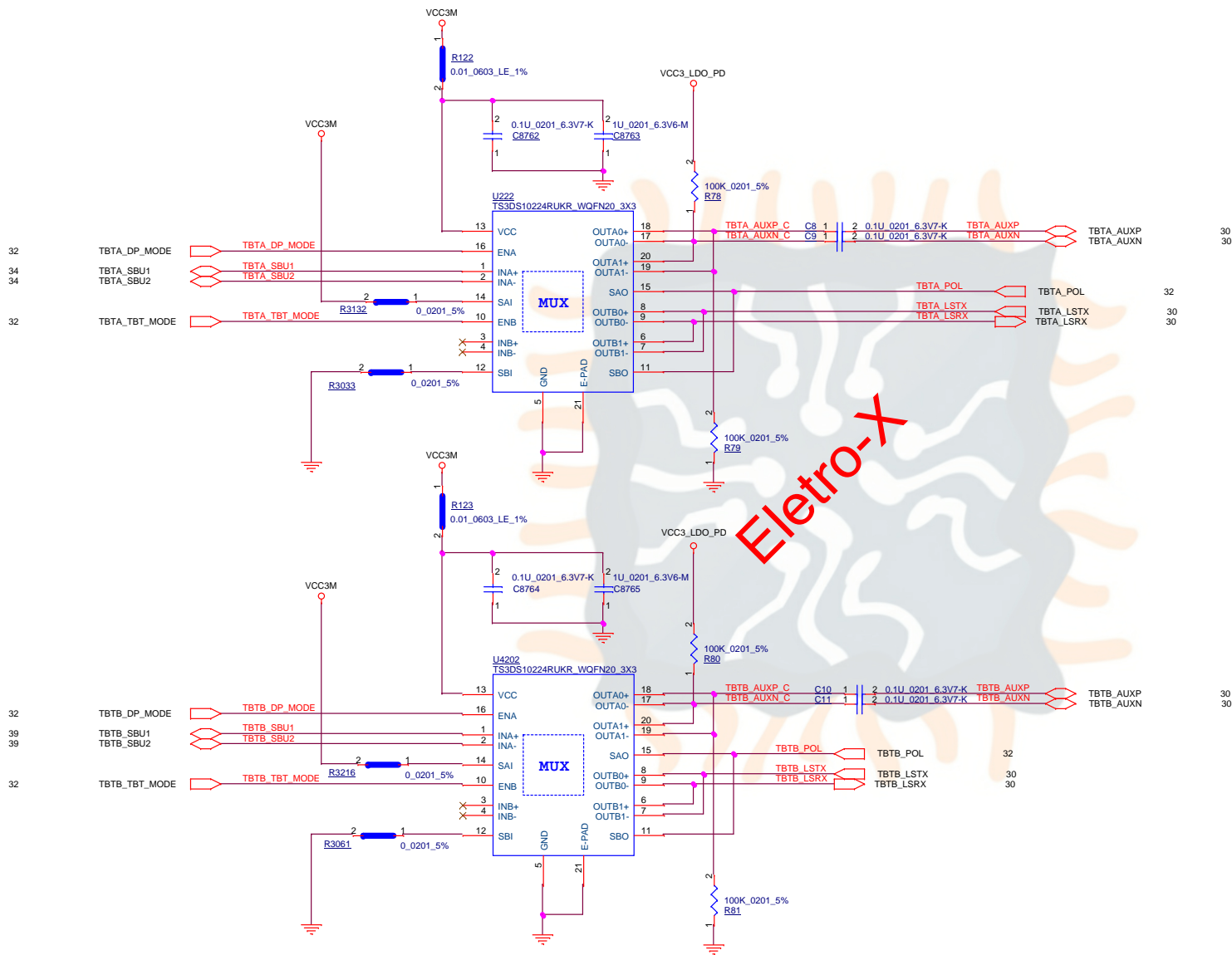


EleTRO-X

Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		2014/07/01	
2017/02/17				HDMI CONNECTOR	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPUTER DIVISION OF RAO CENTRAL EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.				Rev	
				0.01	
				Yoda-2	
				Rev	
		Date		2017/02/17	

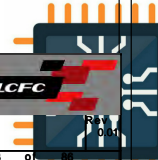


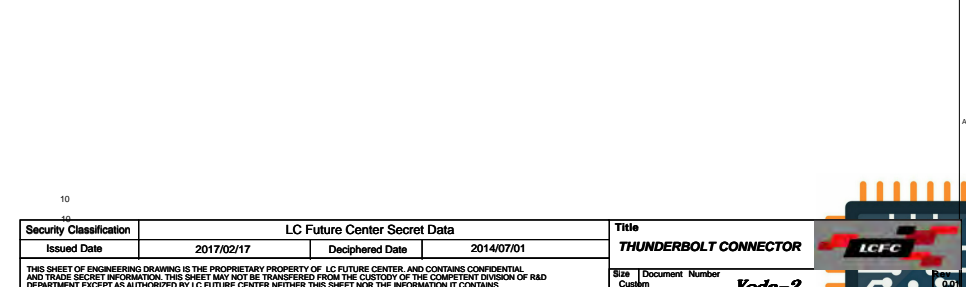
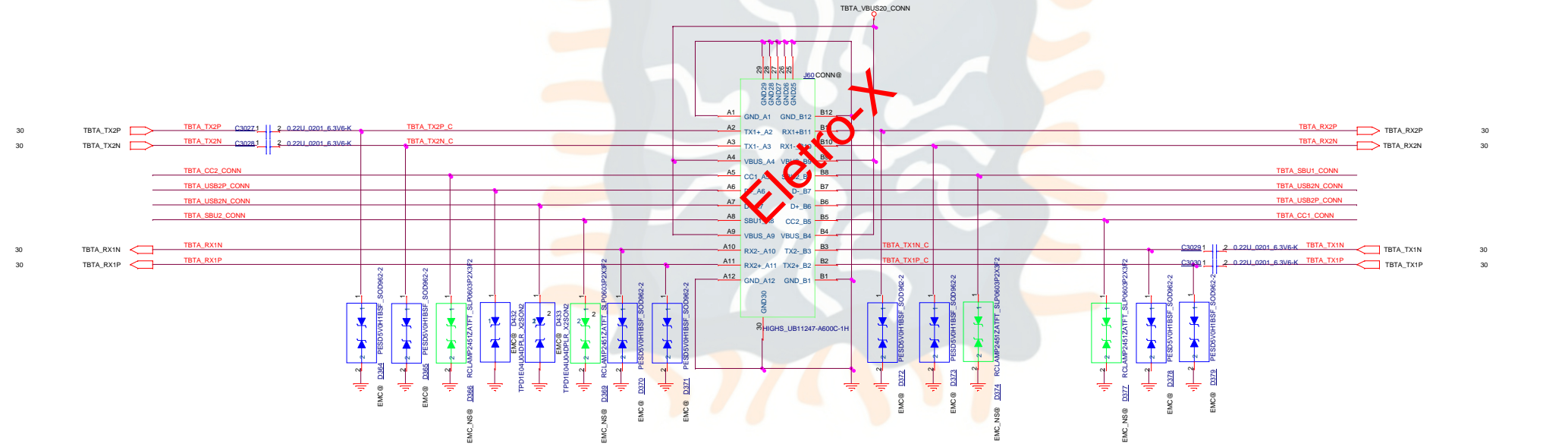
ELECTRO-2



Security Classification		LC Future Center Secret Data	
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			

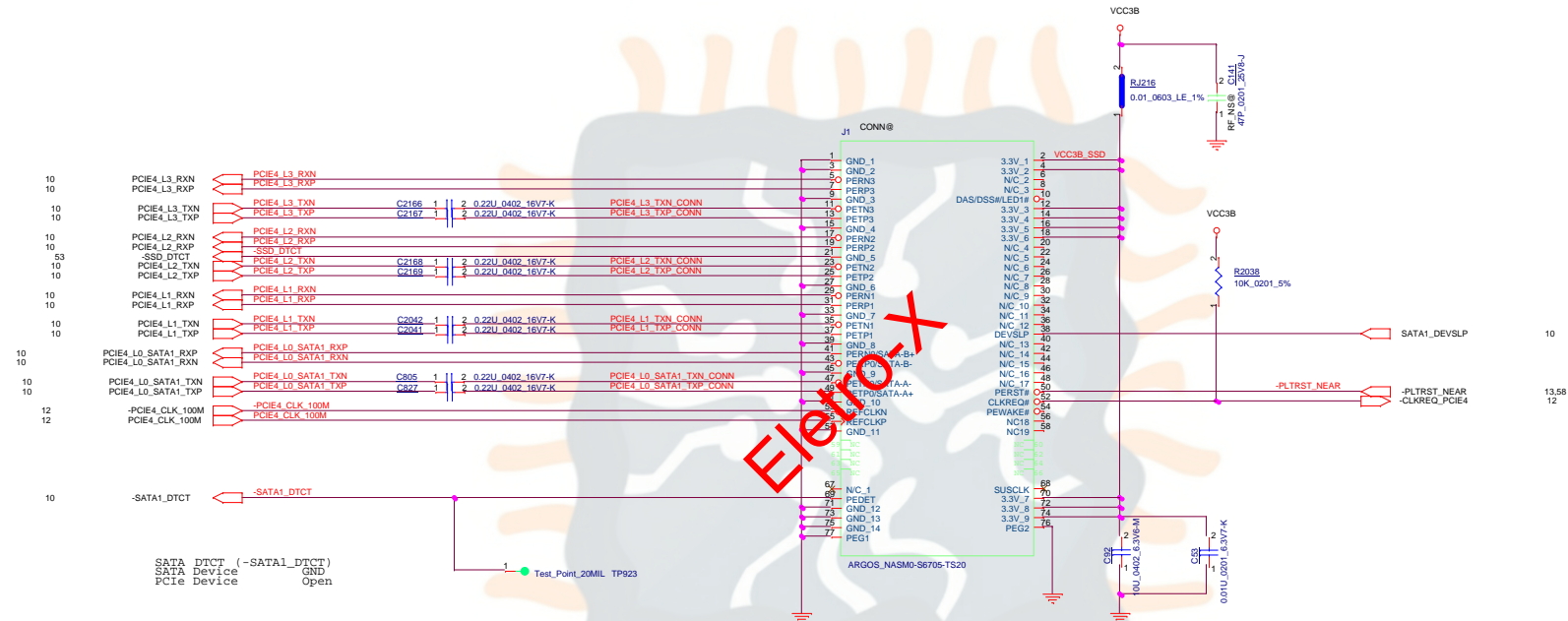
Title		THUNDERBOLT DMUX	
Size	Document Number	Yoda-2	
Custom			
Date:	Friday, November 03, 2017	Sheet	33 of 33





M.2 Socket 3 (Key-M) for 2280 S3 SSD

H=2.00mm Connector



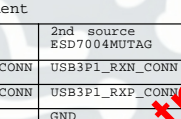
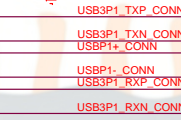
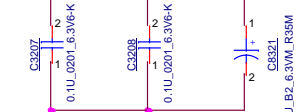
Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	M.2 SOCKET 3 MODULE I/F	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.					
Doc	Document Number	Yoda-2			
Ctrlm	Ctrlm	Friday, November 03, 2017			
Date:	Friday, November 03, 2017	1 Sheet			



ELECTRO-X

USB_PWR_S2

**WIDE PATTERN(MIN 500mA)
PLACE NEAR USB CONN**



D423 Pin assignment

	1st source DF10G5M4N	2nd source ESD7004MUTAG
Pin1	USB3P1_RXN_CONN	USB3P1_RXN_CONN
Pin2	USB3P1_RXP_CONN	USB3P1_RXP_CONN
Pin3	GND	GND
Pin4	USB3P1_TXN_CONN	USB3P1_TXN_CONN
Pin5	USB3P1_TXP_CONN	USB3P1_TXP_CONN
Pin6	USB3P1_TXP_CONN	USB3P1_TXP_CONN
Pin7	USB3P1_TXN_CONN	USB3P1_TXN_CONN
Pin8	-	-
Pin9	USB3P1_RXP_CONN	USB3P1_RXP_CONN
Pin10	USB3P1_RXN_CONN	USB3P1_RXN_CONN

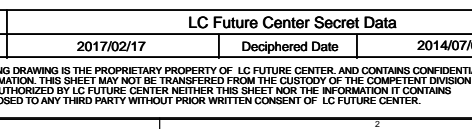
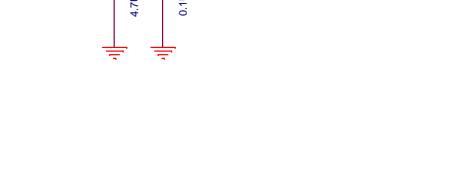
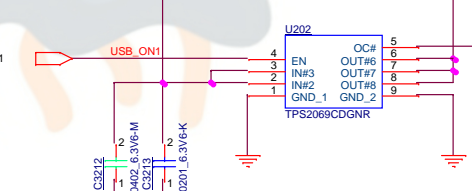
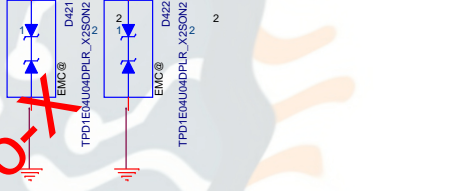
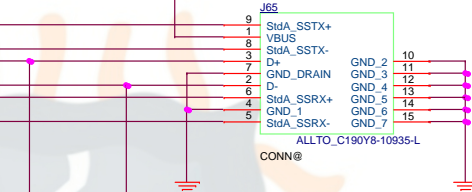
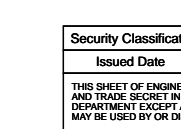
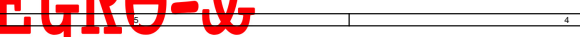
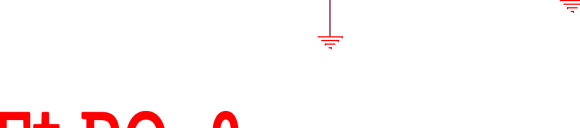
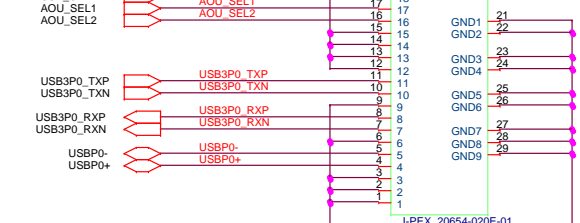
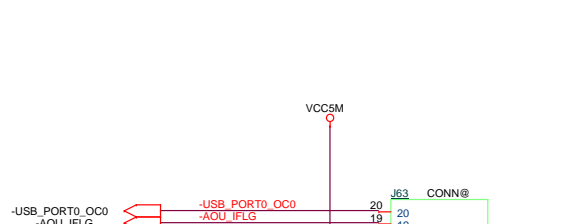
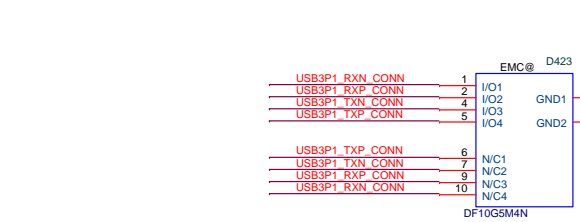
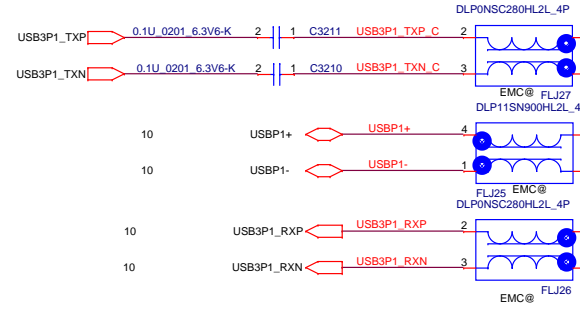


TABLE of USB3.0 Single

TI	TPS2069CDGMR
ROHM	BD82032FVJ-GE2



Security Classification	LC Future Center Secret Data	
Issued Date	2017/02/17	Deciphered Date
		2014/07/01

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.

Title	
USB POWER/CONN	
Size	Document Number
Custom	Yoda-2
Date	Friday, November 03, 2017
Sheet	36

ELETRO-2

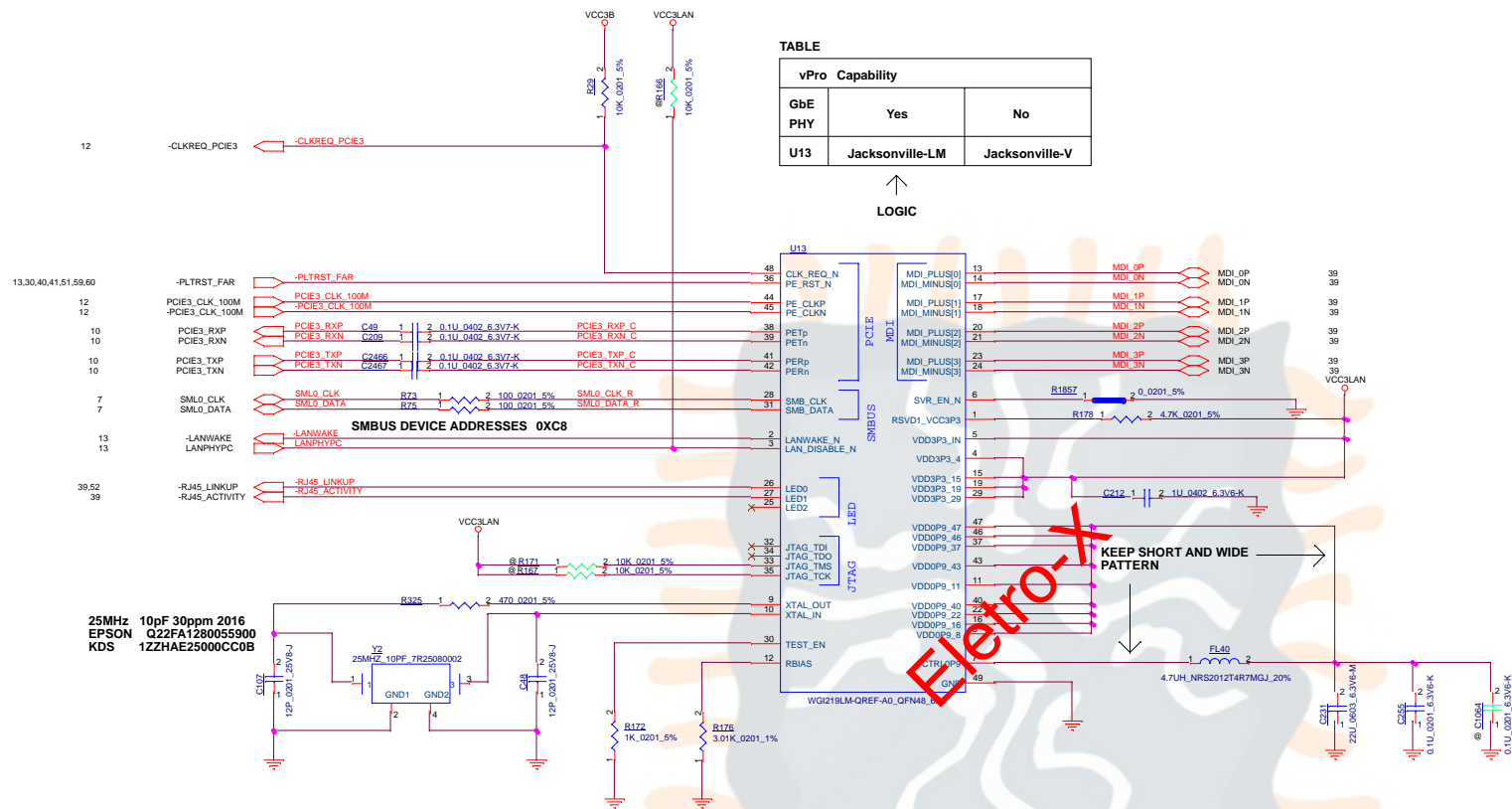
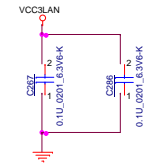
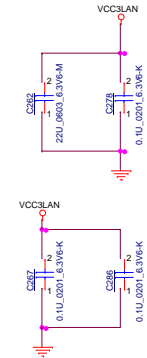


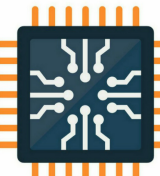
TABLE		
vPro Capability		
GbE PHY	Yes	No
U13	Jacksonville-LM	Jacksonville-V

LOGIC

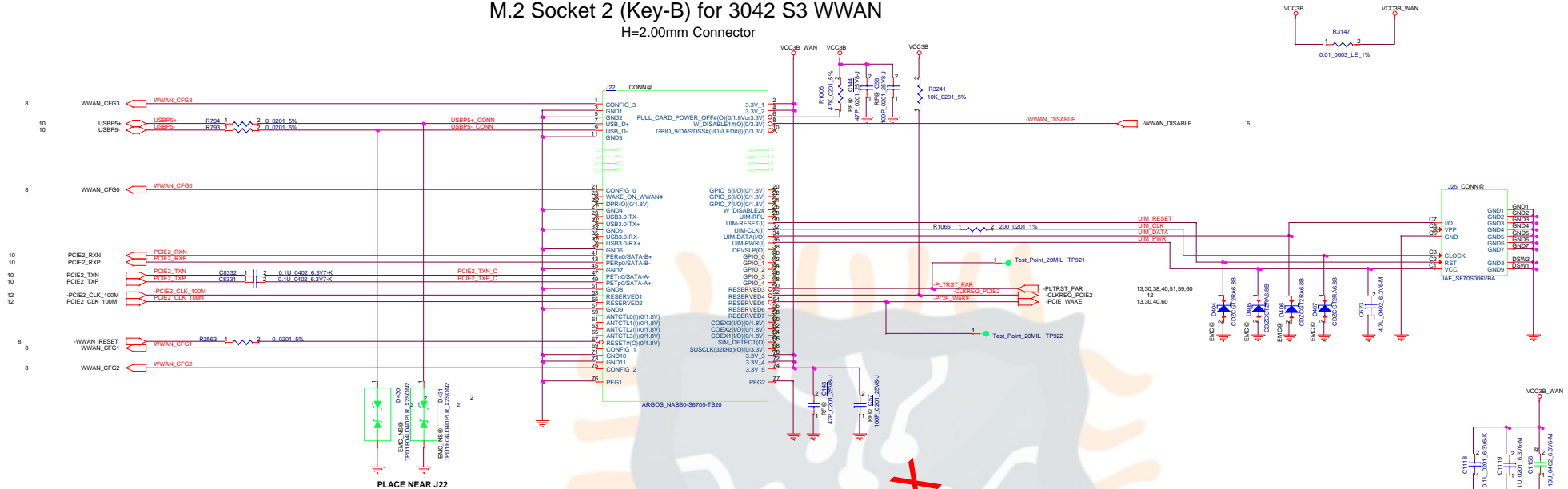


25MHz 10pF 30ppm 2016
EPSON Q22FA1280055900
KDS 1ZZHAE25000CC0B

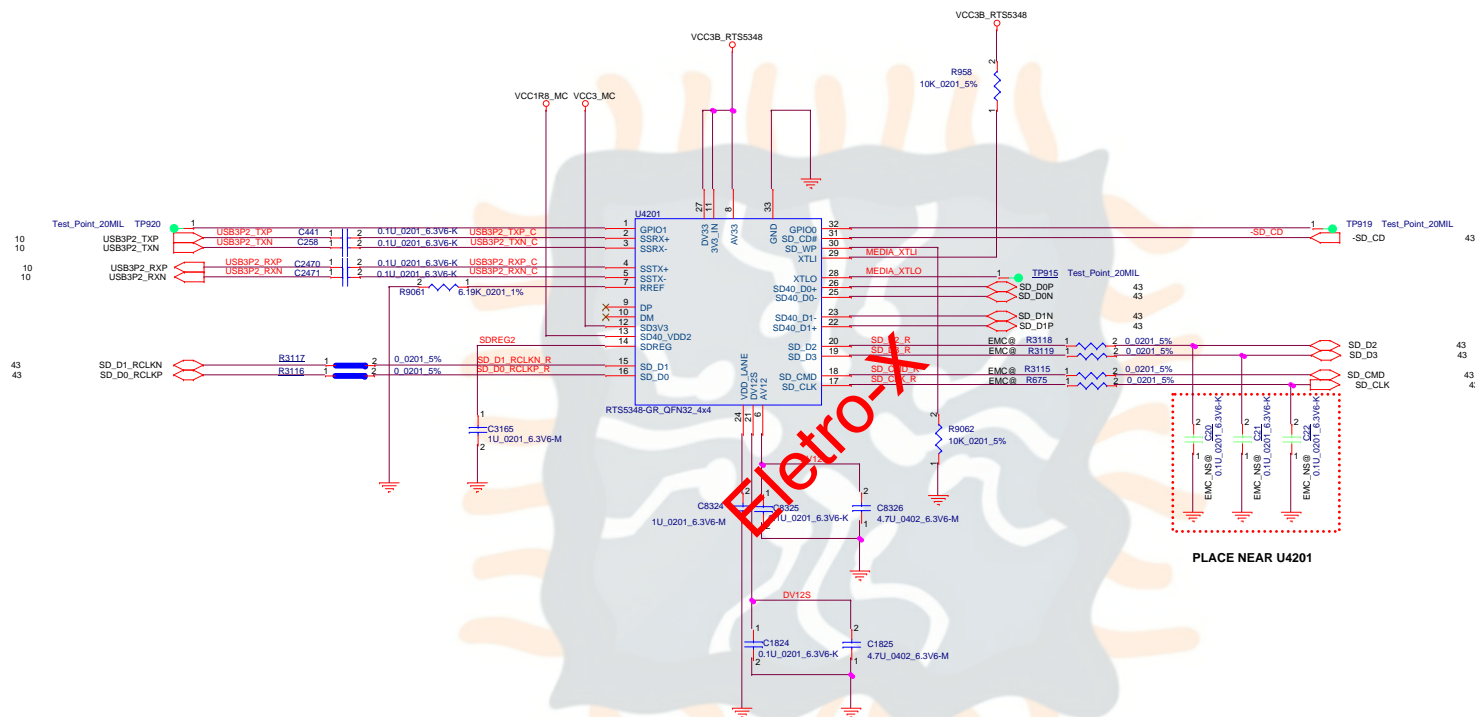
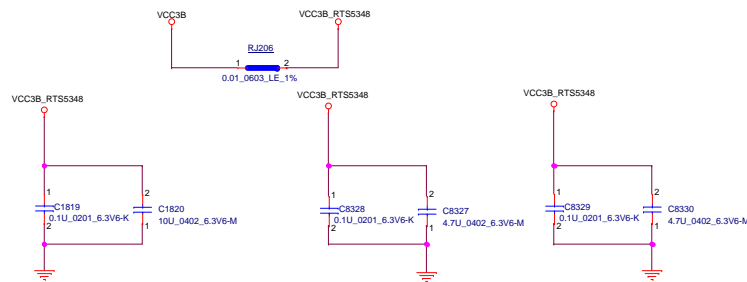
Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			GBE JACKSONVILLE
Rev	0.01	Document Number	Yoda-2
Date	Friday, November 03, 2017	Sheet	38 of 68



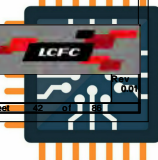
M.2 Socket 2 (Key-B) for 3042 S3 WWAN H=2.00mm Connector



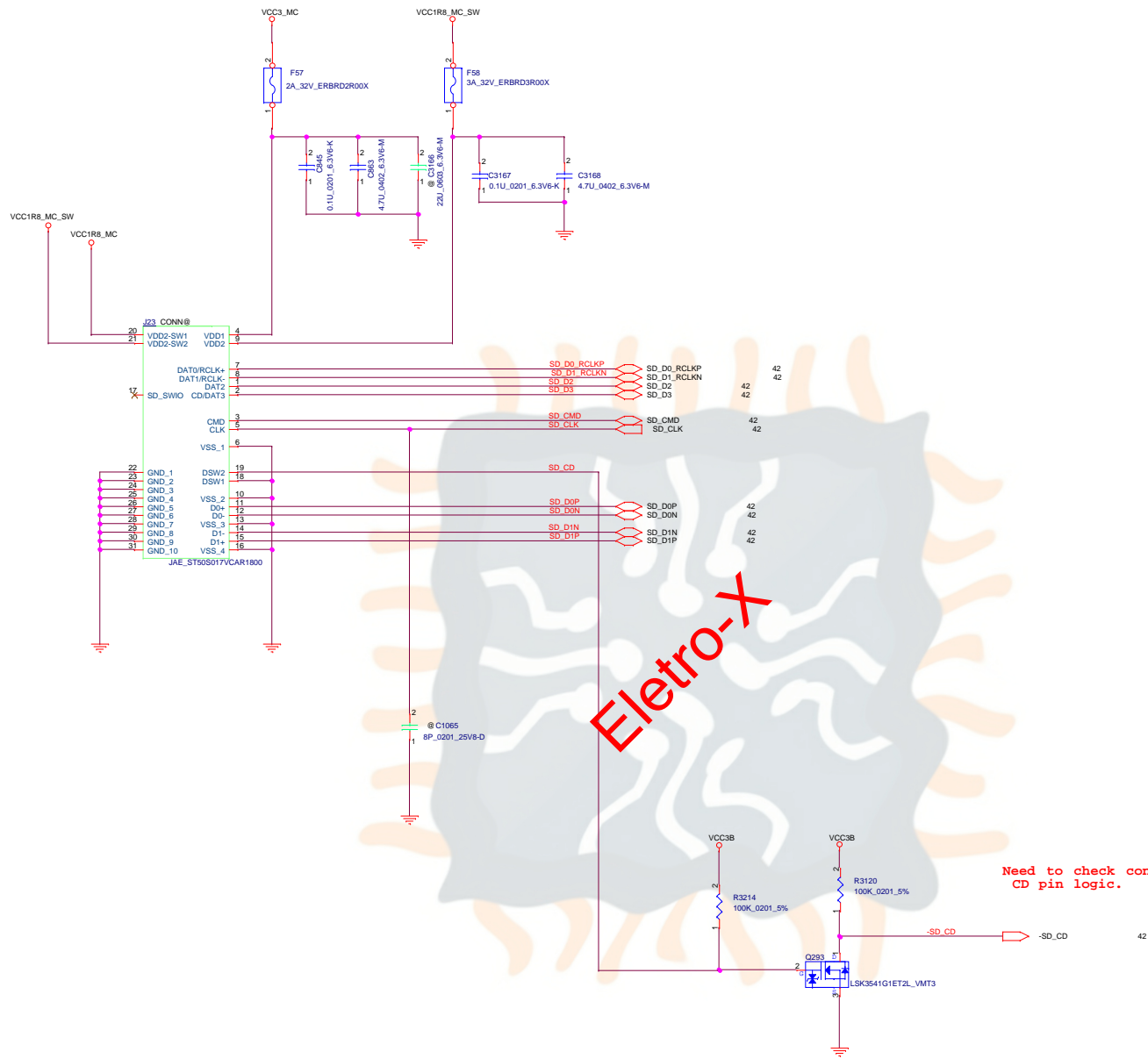
State#	Module Configuration Decodes				Module Type and Main Host Interface	Port Configuration
	CONFIG 0 (Pin 21)	CONFIG 3 (Pin 1)	CONFIG 2 (Pin 75)	CONFIG 1 (Pin 69)		
0	GND	GND	GND	GND	SSD - SATA	N/A
1	GND	GND	GND	NC	SSD - PCIe	N/A
2	GND	GND	NC	GND	WWAN - PCIe	0
3	GND	GND	NC	NC	WWAN - PCIe	1
4	GND	NC	GND	GND	WWAN - PCIe, USB 3.1 Gen1	0
5	GND	NC	GND	NC	WWAN - PCIe, USB 3.1 Gen1	1
6	GND	NC	NC	GND	WWAN - PCIe, USB 3.1 Gen1	2
7	GND	NC	NC	NC	WWAN - PCIe, USB 3.1 Gen1	3
8	NC	GND	GND	GND	WWAN - SSIC	0
9	NC	GND	GND	NC	WWAN - SSIC	1
10	NC	GND	NC	GND	WWAN - SSIC	2
11	NC	GND	NC	NC	WWAN - SSIC	3
12	NC	NC	GND	GND	WWAN - PCIe	2
13	NC	NC	GND	NC	WWAN - PCIe	3
14	NC	NC	NC	GND	WWAN - PCIe, USB 3.1 Gen1	Vendor Defined
15	NC	NC	NC	NC	No Module Present	N/A



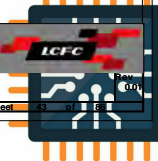
Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	MEDIA CARD CONTROLLER	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.				Size	Document Number
				Caution	Yoda-2
				Date:	Friday, November 03, 2017



ELECTRO-2



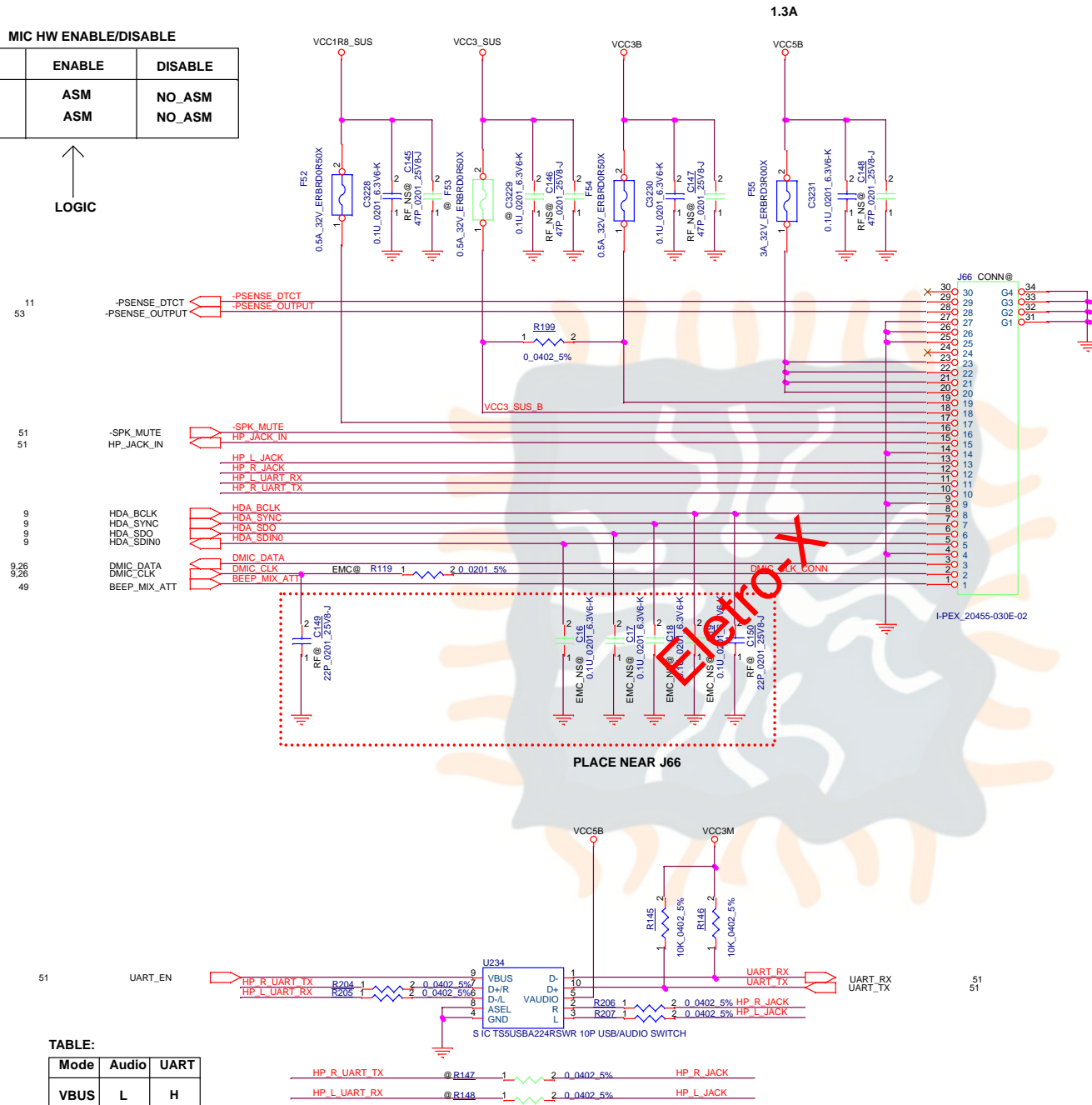
Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			
Size	Document Number	Yoda-2	
Custm		Friday, November 03, 2017	
Date:	Friday, November 03, 2017	Sheet	1



ELECTRO-X

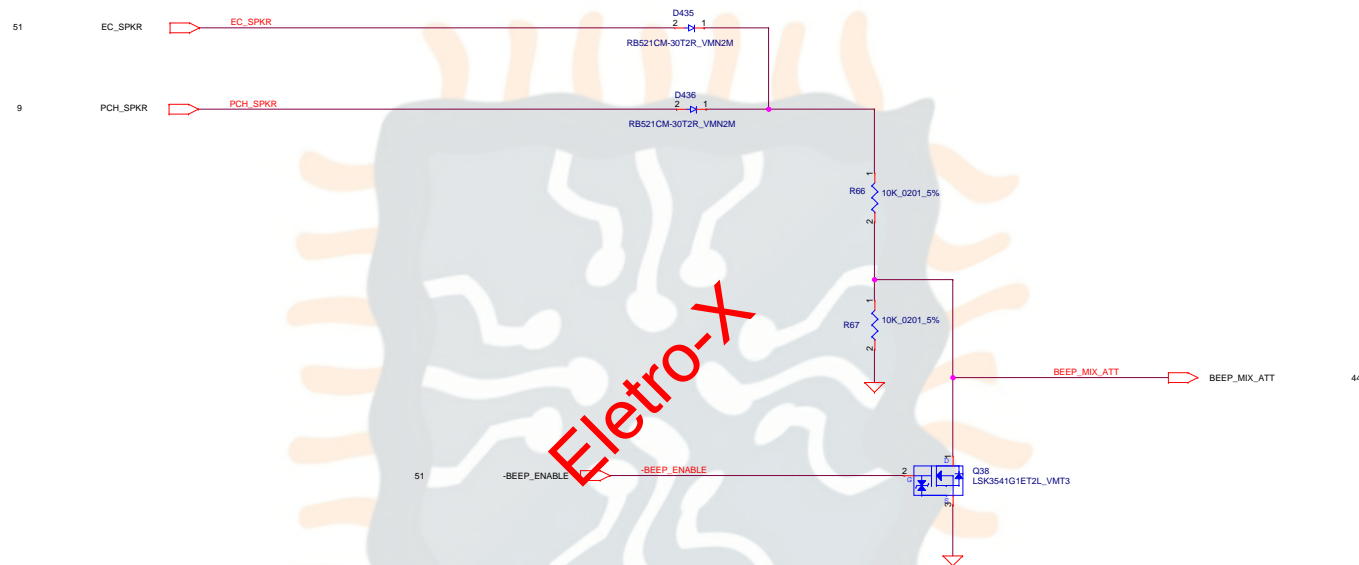
TABLE MIC HW ENABLE/DISABLE

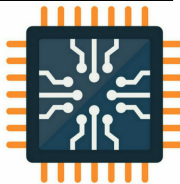
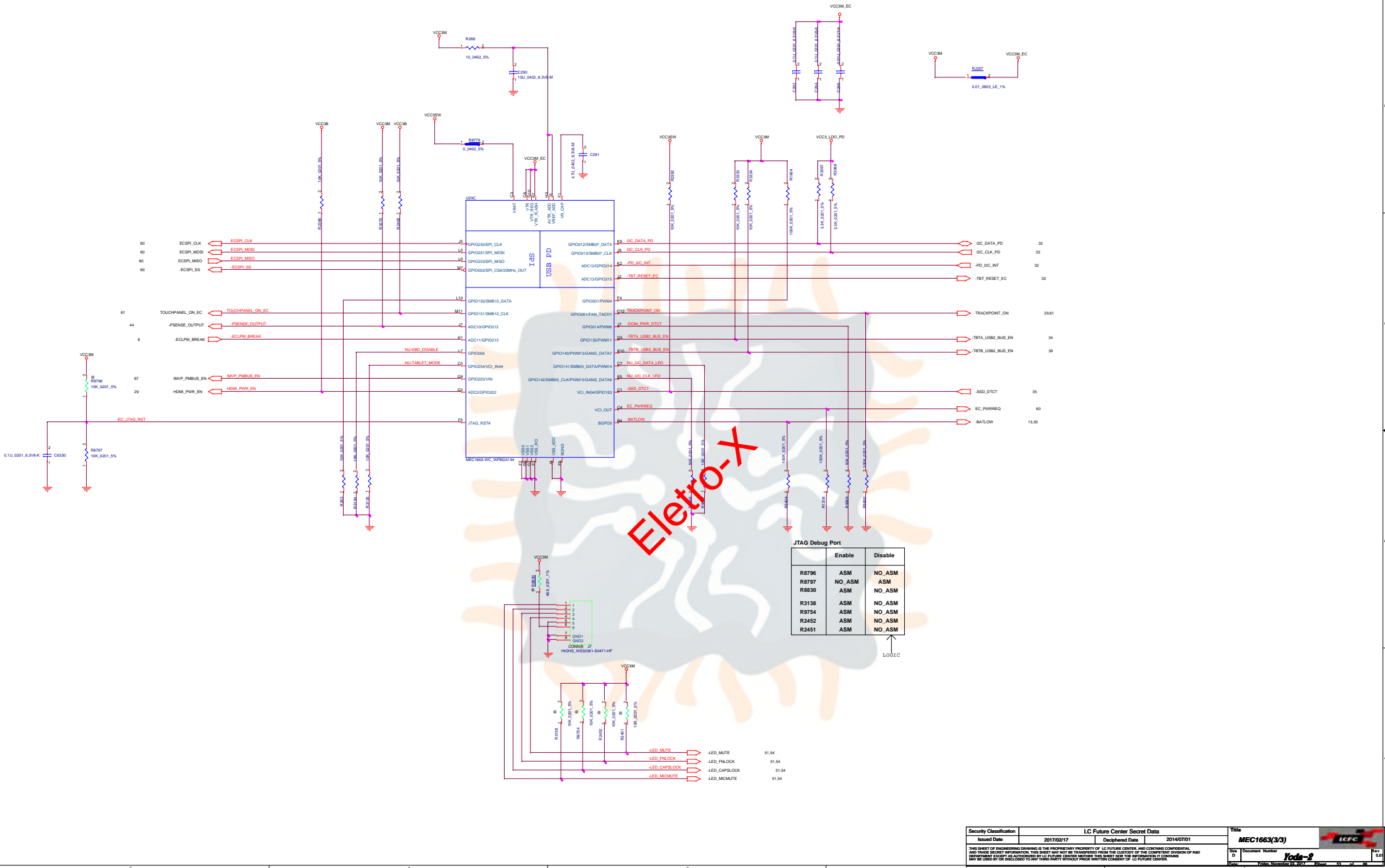
	ENABLE	DISABLE
R961	ASM	NO_ASM
R119	ASM	NO_ASM

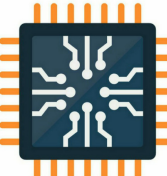
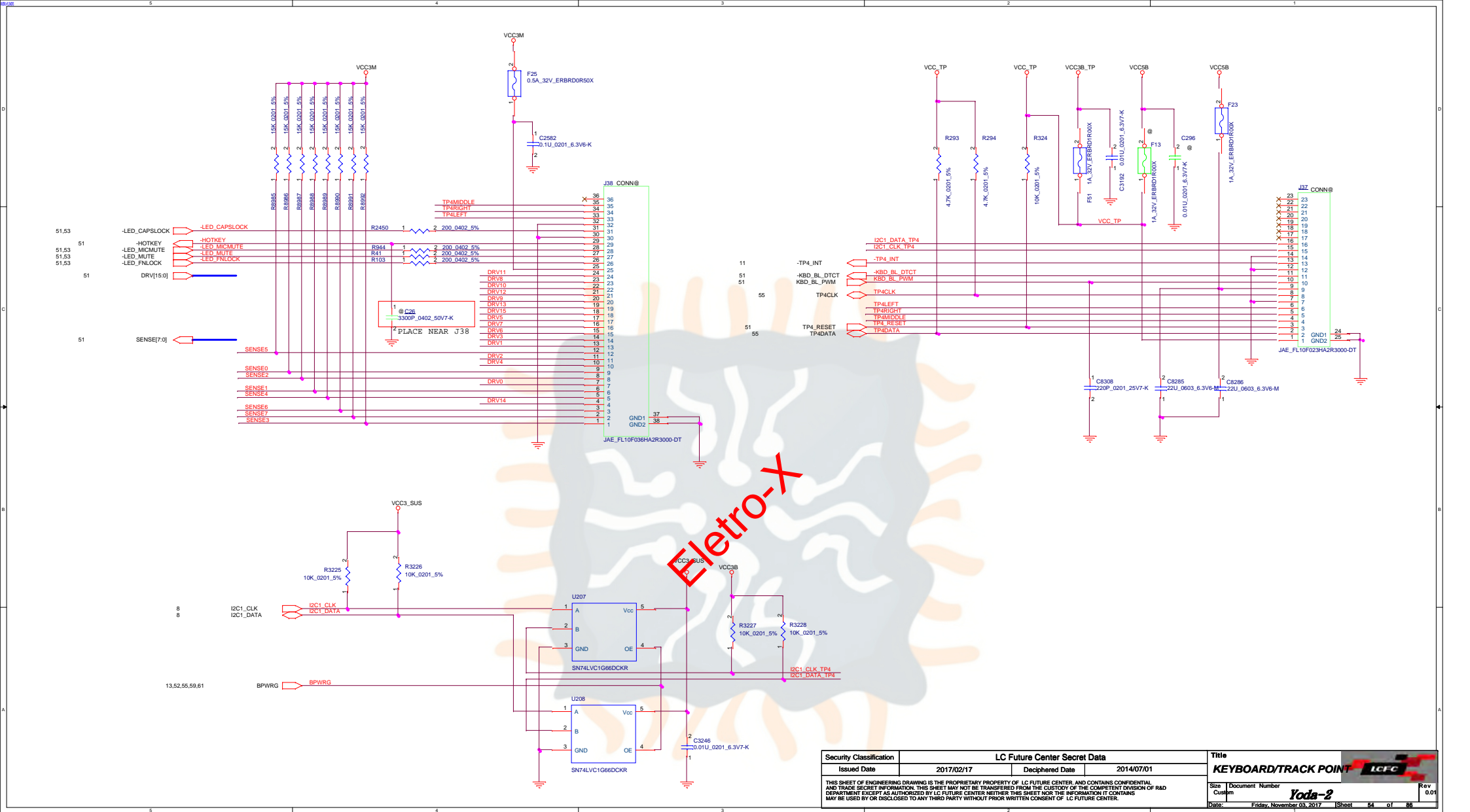
↑
LOGIC

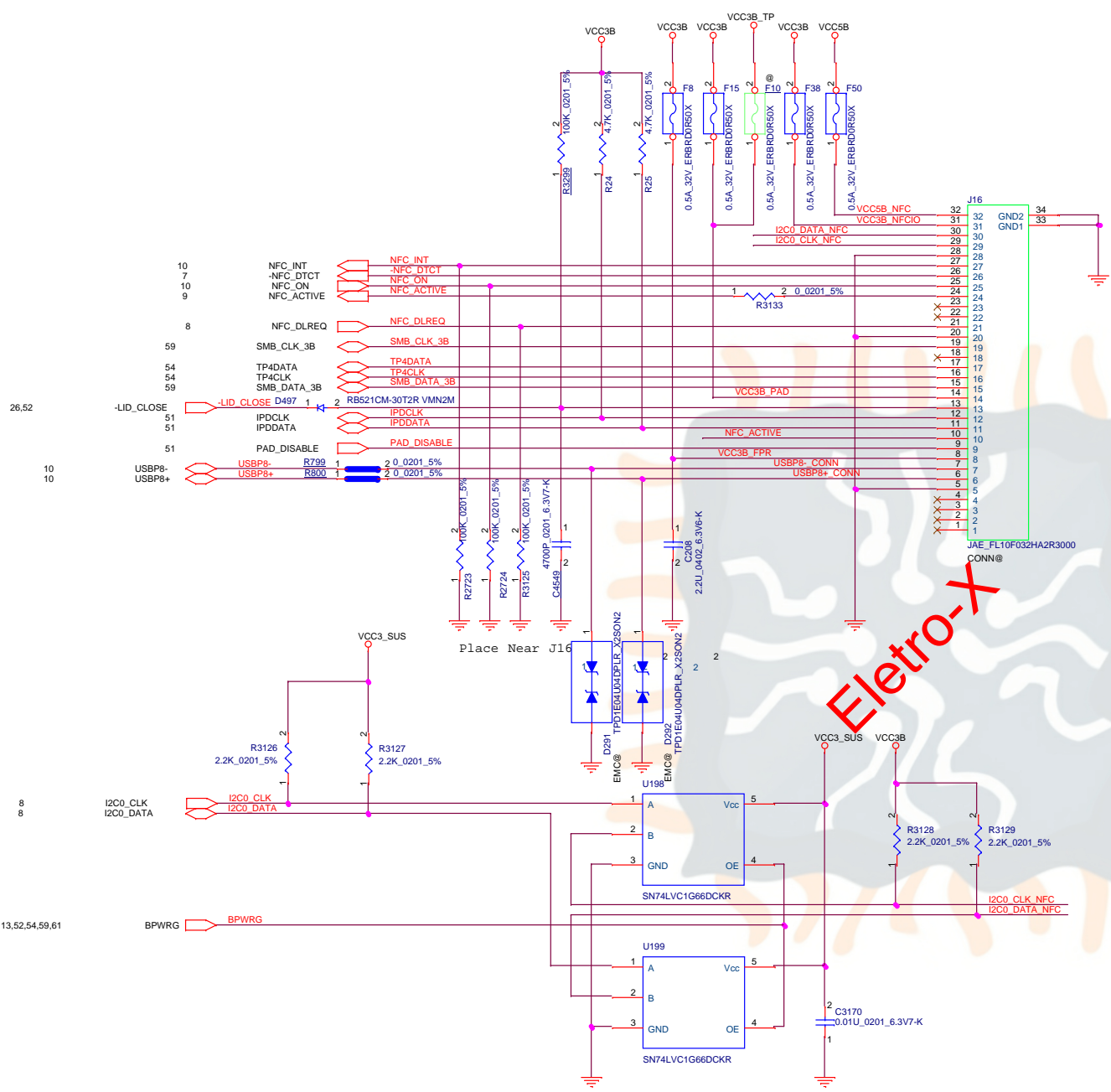
Security Classification	LC Future Center Secret Data	
Issued Date	2017/02/17	Deciphered Date
		2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.		

Title		LCFC	
AUDIO SUBCARD CONNECTOR I/F		Yoda-2	
Size	Document Number	Friday, November 03, 2017	
Custom		Sheet 44 of 44	







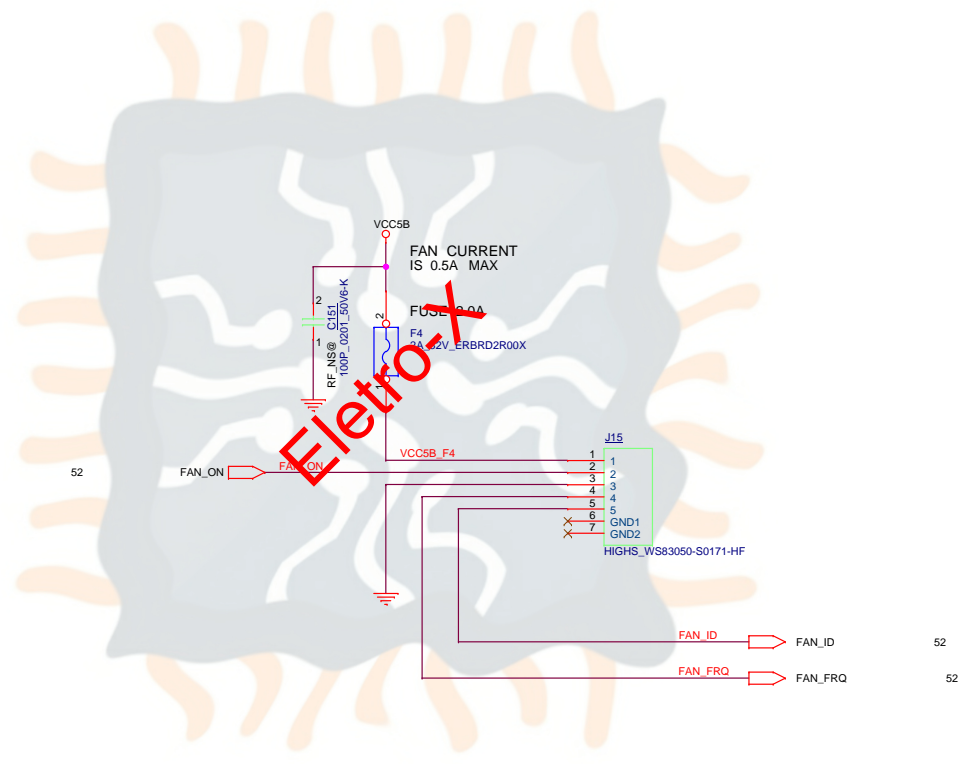


Security Classification		LC Future Center Secret Data	
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			

Title	
TOUCH PAD/NFC/FPR	
Size	Document Number
Custom	Yoda-2
Date:	Friday, November 03, 2017
Sheet	55

Eletr-X

ELECTRO-2



TABLE

P/N	ADDR_SEL	Address
BMA255	H	32h (W) & 33h (R)
	L	30h (W) & 31h (R)
KX022-1020	H	3Eh (W) & 3Fh (R)
	L	3Ch (W) & 3Dh (R)

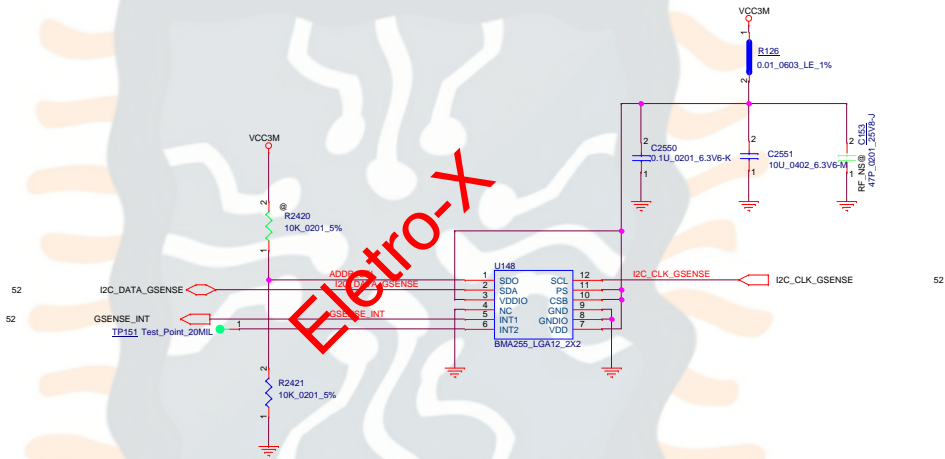
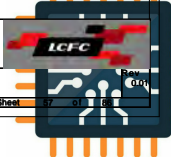
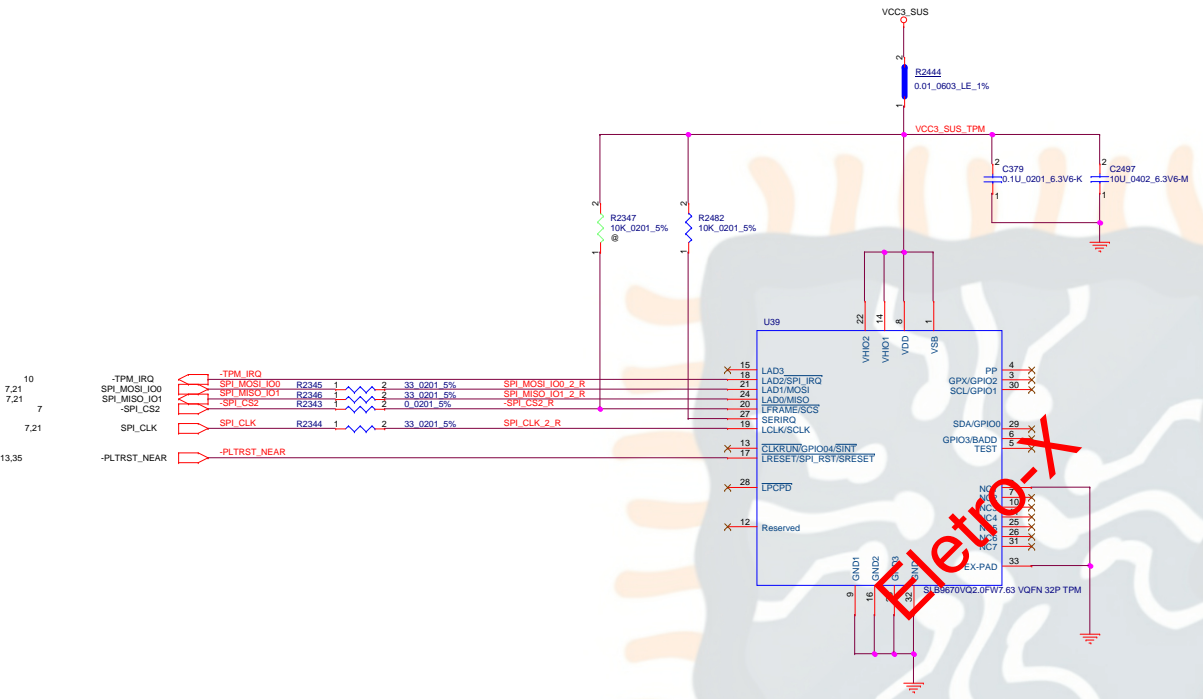


TABLE of G-Sensor (U148)		
Vendor	P/N	LCFC P/N
BOSCH	BMA255	SA00005YJ00
Kionix	KX022-1020	SA000081E00





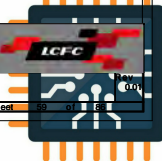
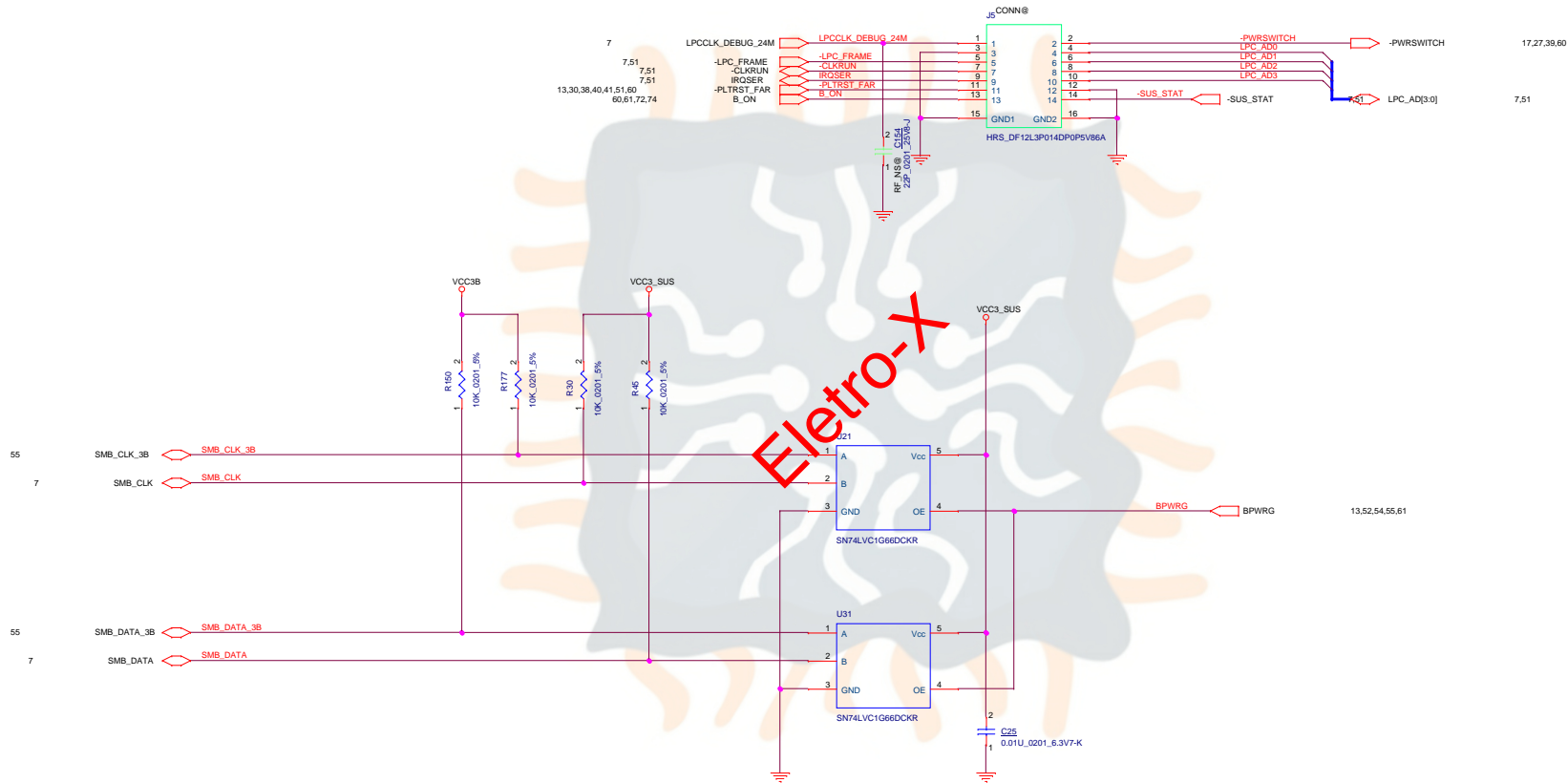
TABLE

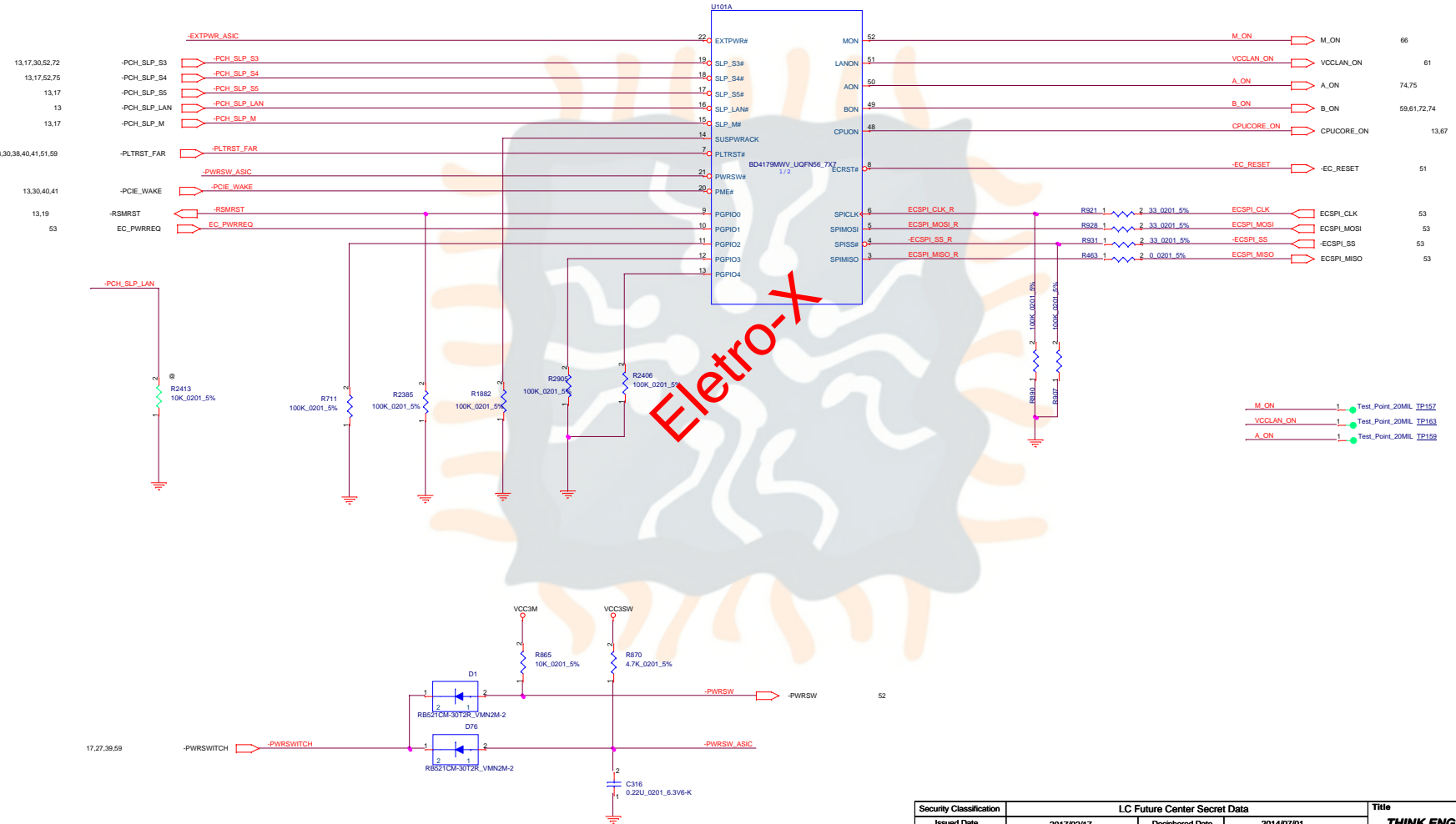
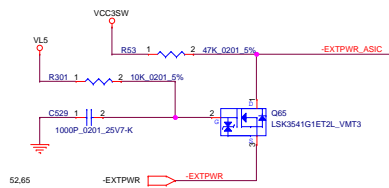
Pin No	TCG PTP Spec (v38)	Infineon SLB9670VQ2.0 FW 7.63	ST Micro ST33HTPH2E32AHB4
1	VDD	VDD	NC
2	GND	GND	GND
3	GPIO	NC	NC
4	GPIO	NC	PP
5	NC	NC	NC
6	VNC/GPIO	GPIO	NC
7	GPIO/VDD	PP	GPIO
8	VDD	VDD	NC
9	GND	GND	NC
10	VNC	NC	NC
11	NC	NC	NC
12	NC	NC	NC
13	VNC/GPIO	NC	NC
14	VDD	NC	NC
15	NC	NC	NC
16	GND	NC	NC
17	SPI_RST#	RST#	SPI_RST#
18	SPI_PIRQ#	PIRQ#	SPI_PIRQ#
19	SPI_CLK	SCLK	SPI_CLK
20	SPI_CS#	CS#	SPI_CS#
21	MOSI	MOSI	MOSI
22	VDD	VDD	VPS
23	GND	GND	NC
24	MISO	MISO	MISO
25	NC	NC	NC
26	NC	NC	NC
27	NC	NC	NC
28	NC	NC	NC
29	VNC/GPIO	NC	NC
30	VNC/GPIO	NC	NC
31	VNC	NC	NC
32	GND	GND	NC


ELECTRO-X

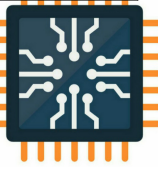
Table with 3 columns: REF DES, ENABLE, DISABLE. Rows include J5, R220, and R220.

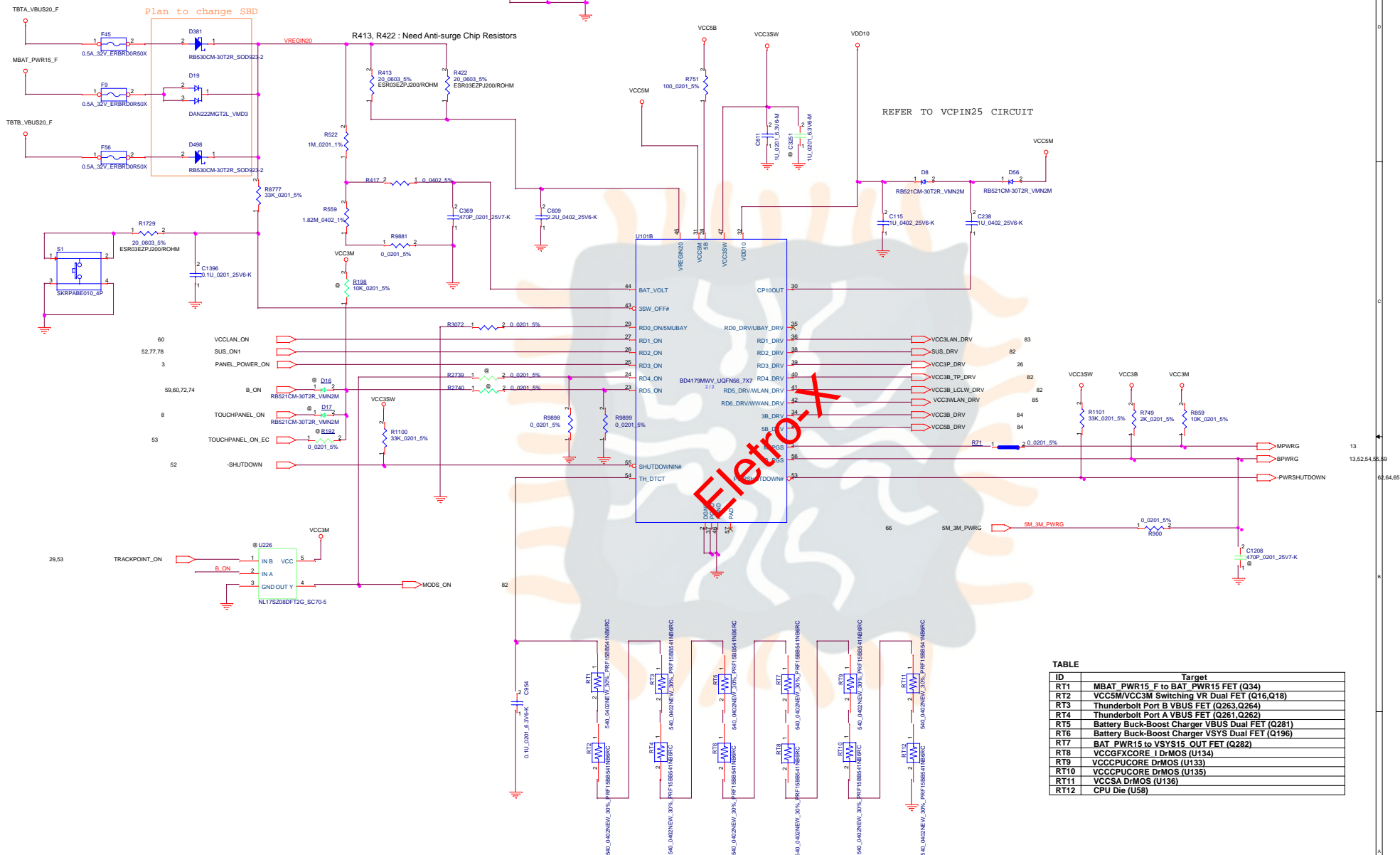
LOGIC



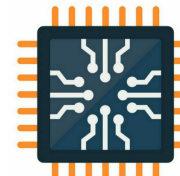


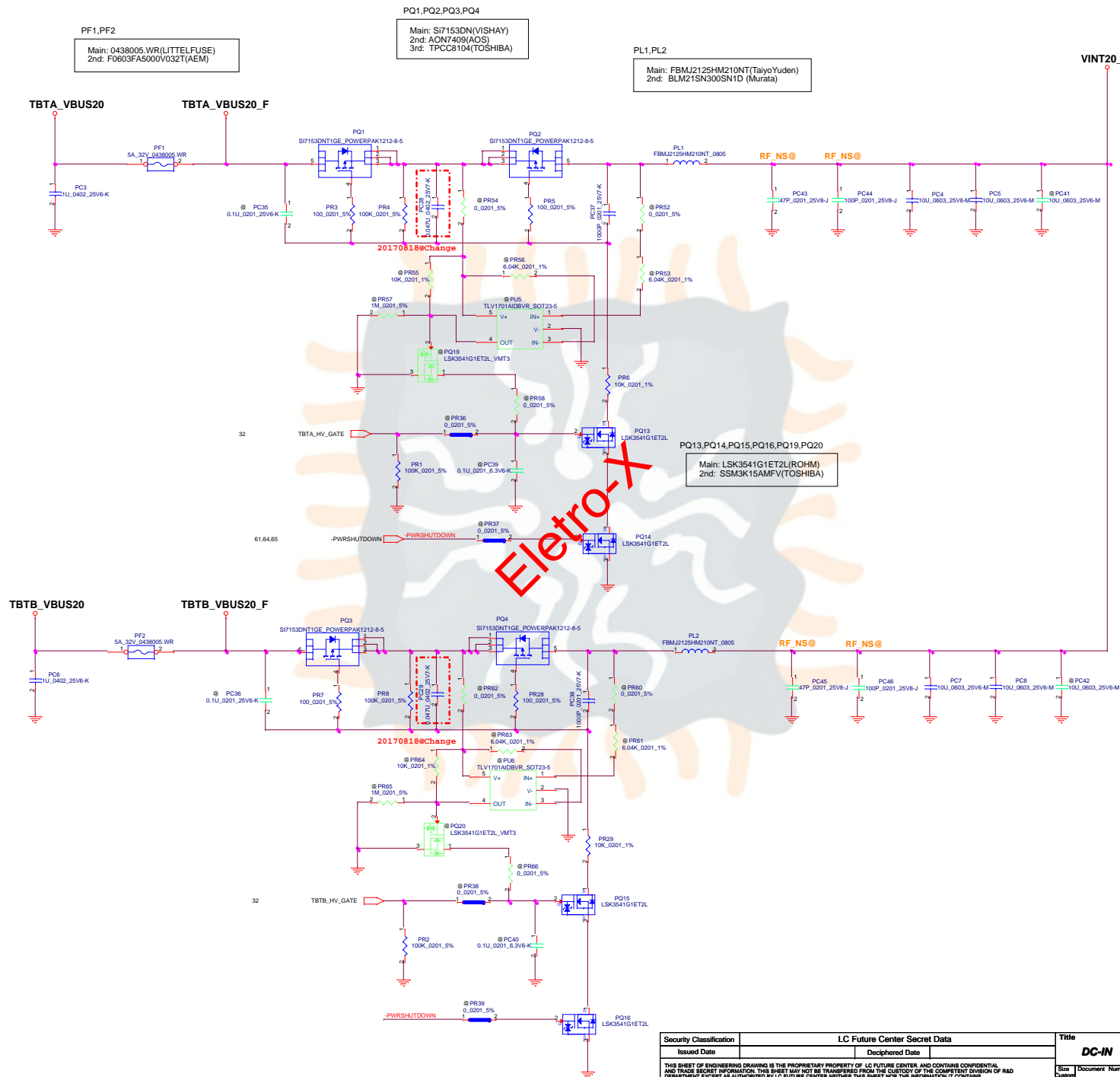
Security Classification		LC Future Center Secret Data		Title	
Issued Date	2017/02/17	Deciphered Date	2014/07/01	THINK ENGINE-3(1/2)	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.					
Size	Document Number	Yoda-2		Rev	0.01
Custm					
Date	Friday, November 03, 2017	Sheet	60	of	68





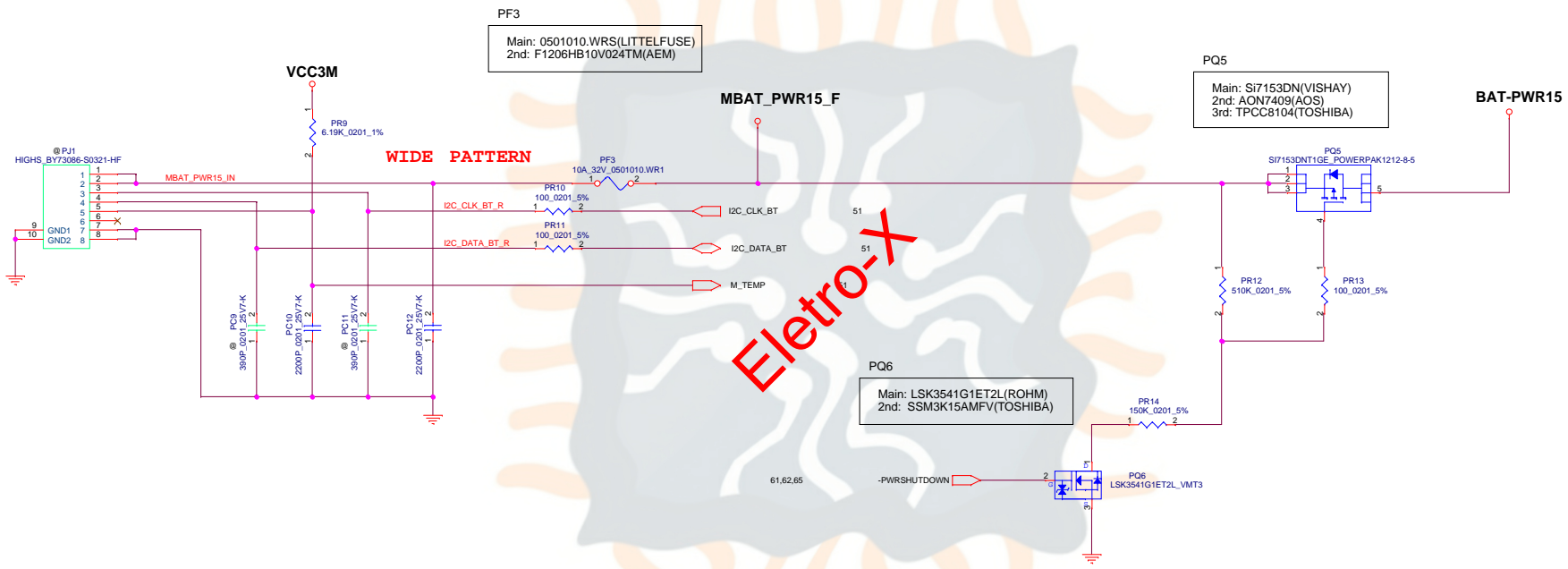
ID	Target
RT1	MBAT_PWR15_F to BAT_PWR15_FET (Q34)
RT2	VCC5M/VCC3M Switching VR Dual FET (Q16,Q18)
RT3	Thunderbolt Port B VBUS FET (Q263,Q264)
RT4	Thunderbolt Port A VBUS FET (Q261,Q262)
RT5	Battery Buck-Boost Charger VBUS Dual FET (Q281)
RT6	Battery Buck-Boost Charger VSYS Dual FET (Q196)
RT7	BAT_PWR15 to VSYS15_OUT FET (Q282)
RT8	VCCGFXCORE_1 DrMOS (U134)
RT9	VCCCPUCORE DrMOS (U133)
RT10	VCCCPUCORE DrMOS (U135)
RT11	VCCSA DrMOS (U136)
RT12	CPU Die (U58)





Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		DC-IN	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.					
Size	Document Number	Rev	Yoda-2		
Date:	Friday, November 03, 2017	1	Sheet	62	of 66





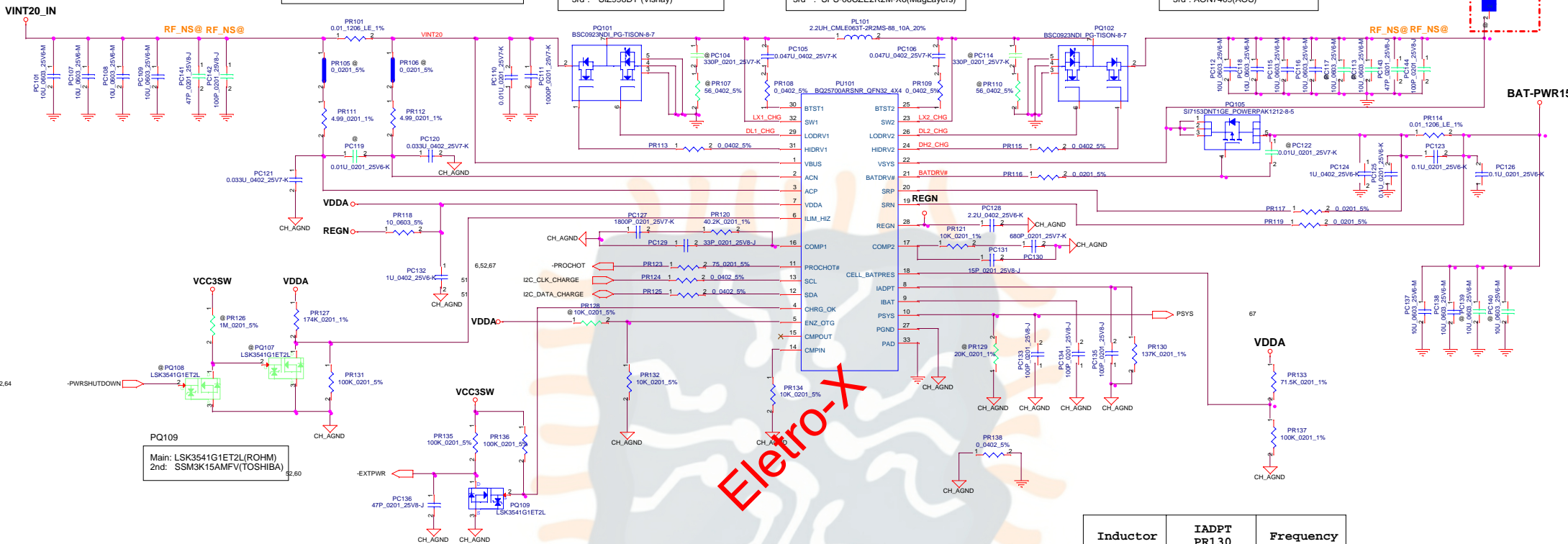


TABLE: ILIM_HIZ

IDPM	V (ILIM)	PR127
500mA	1.2V	402K
1.0A	1.4V	332K
1.5A	1.6V	280K
2.0A	1.8V	237K
3.0A	2.2V	174K
3.25A	2.3V	162K

← LOGIC

TABLE: CELL BATPRES

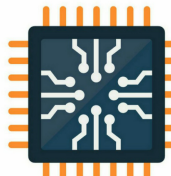
# of CELL	VCELL PRES	PR133
1-CELL	1.5V	301K
2-CELL	2.5V	140K
3-CELL	3.5V	71.5K
4-CELL	4.5V	33.2K

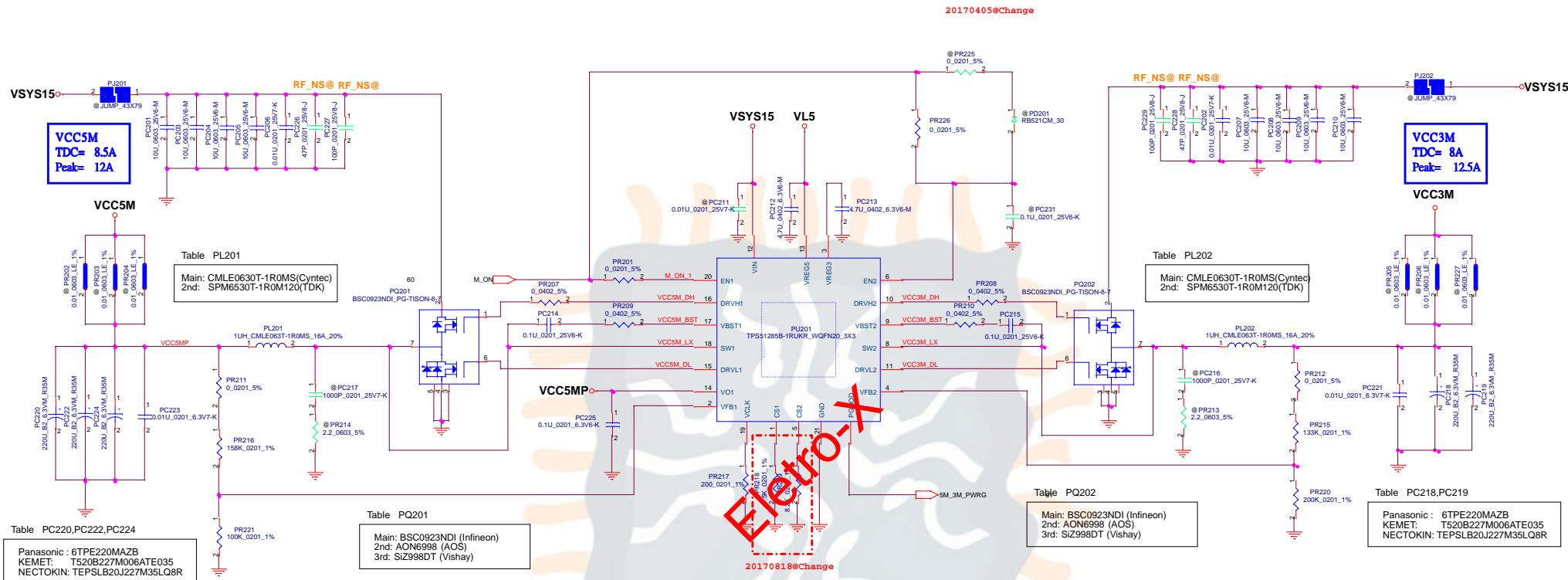
← LOGIC

TABLE

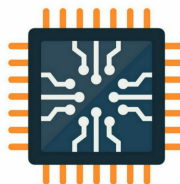
Inductor	IADPT PR130	Frequency
1.0uH	93kohm	800kHz
1.5uH	108kohm	800kHz
2.2uH	137kohm	800kHz
3.3uH	168kohm	800kHz

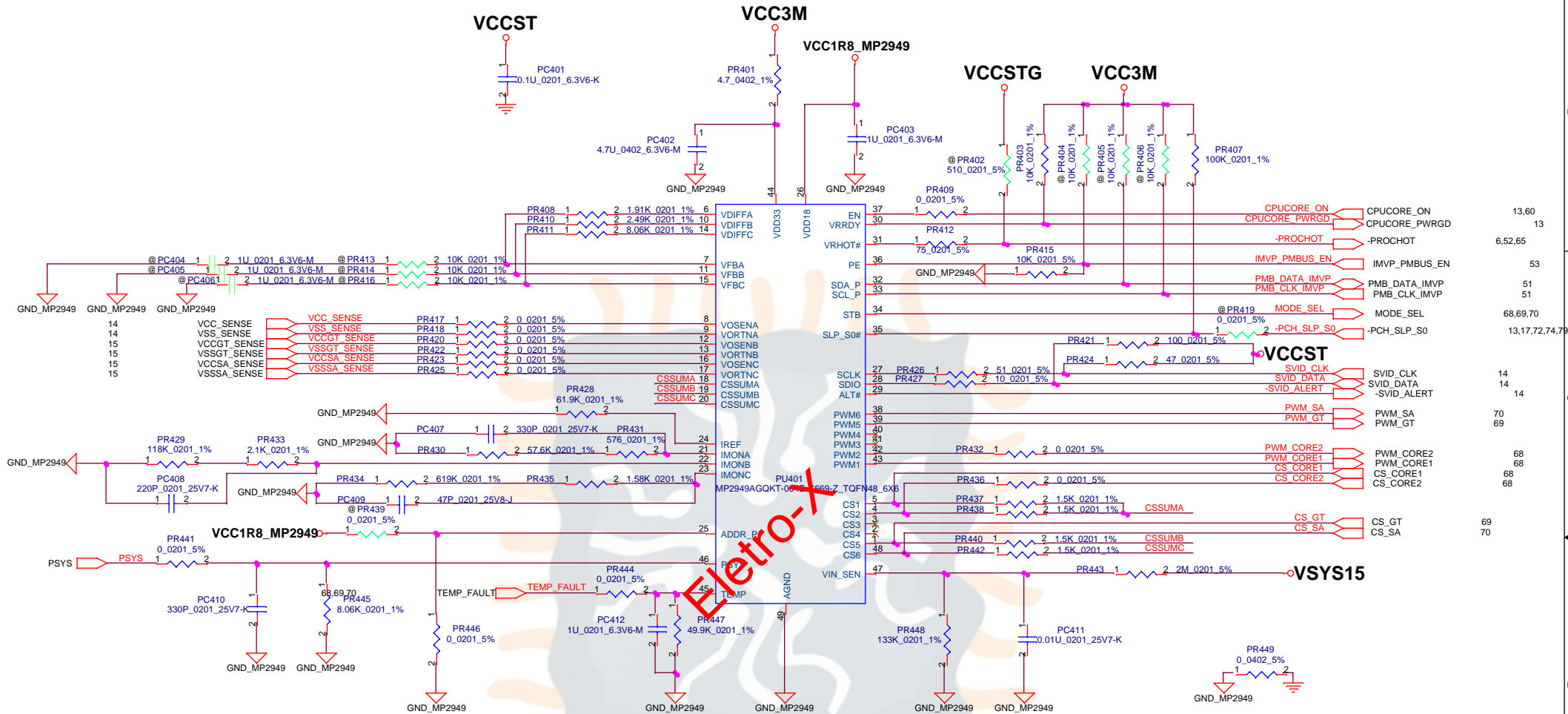
Security Classification	LC Future Center Secret Data		Title
Issued Date	Deciphered Date		BATTERY CHARGER(BQ25700A)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			Size Custom
			Document Number Yoda-2
			Date Friday, November 03, 2017
			Sheet 05 of 06



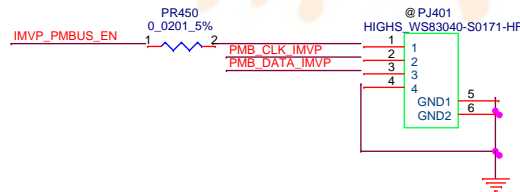


Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		DC/DC VCC5M/VCC3M(TP551285B-1)	
<p>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.</p>					
Size		Document Number		Rev	
0.01		Yoda-2		0.01	
Date:		Friday, November 03, 2017		Sheet 66 of 66	

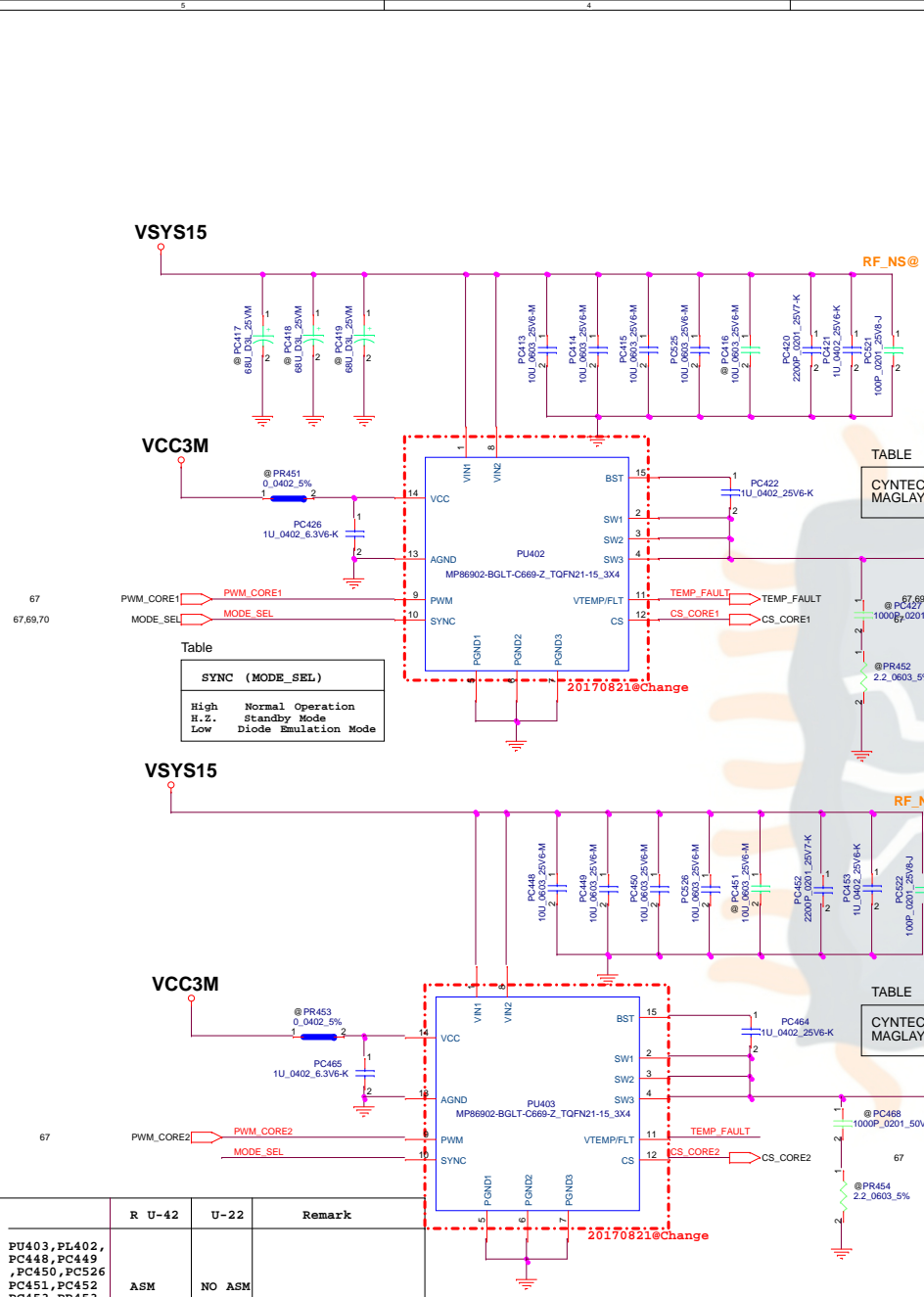




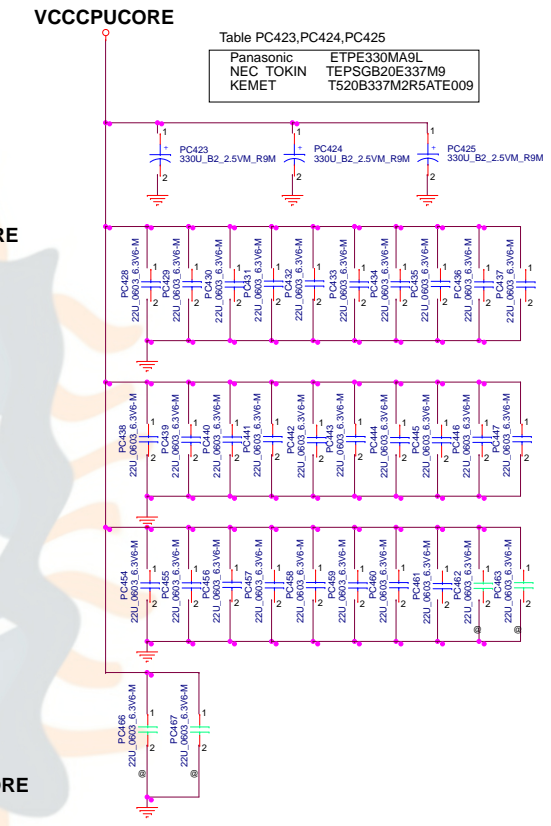
	R U42	U22
PU401	MP2949AGQKT-004E-C669-Z	MP2949AGQKT-004F-C669-Z
PR432	0_0201_5%	NA
PR436	0_0201_5%	NA
PR438	1.5K_0201_1%	NA
PR431	576_0201_1%	1.37K_0201_1%
PR430	57.6K_0201_1%	115K_0201_1%
PC407	330P_0201_25V7-K	220P_0201_25V7-K
PR439	NA	2.8K_0201_1%
PR446	0_0201_5%	20K_0201_1%

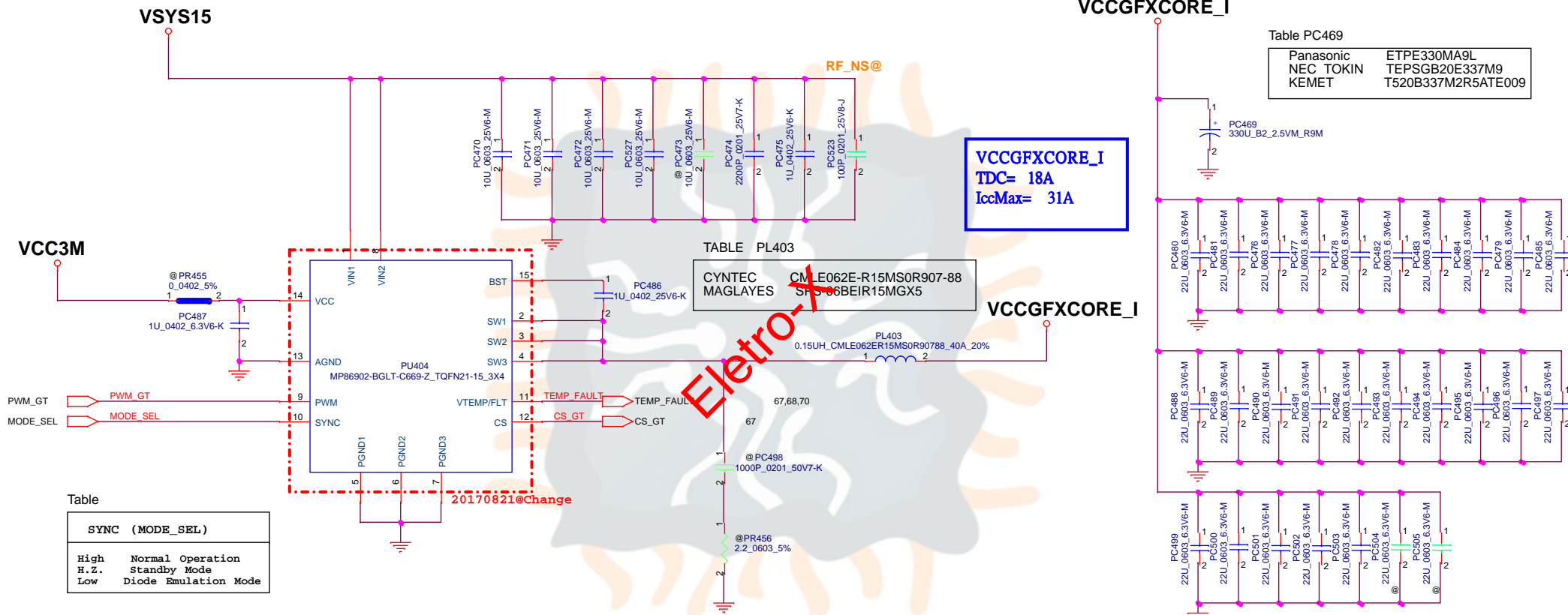


MFR_PHASE_CFG[4:0]	Active PWM Pins		
	Rail A	Rail B	Rail C
00100b	1,2,3,4	5	6
00111b	1,2,3	4,5	6
01001b	1,2,3	5	6
01100b	1,2	4,5	6
01110b	1,2	5	6
10001b	1	5	6



3pcs 330uF + 28pcs for KBL-R U42 VCCCPUCORE
1pcs 330uF + 25pcs for KBL U22 VCCCPUCORE





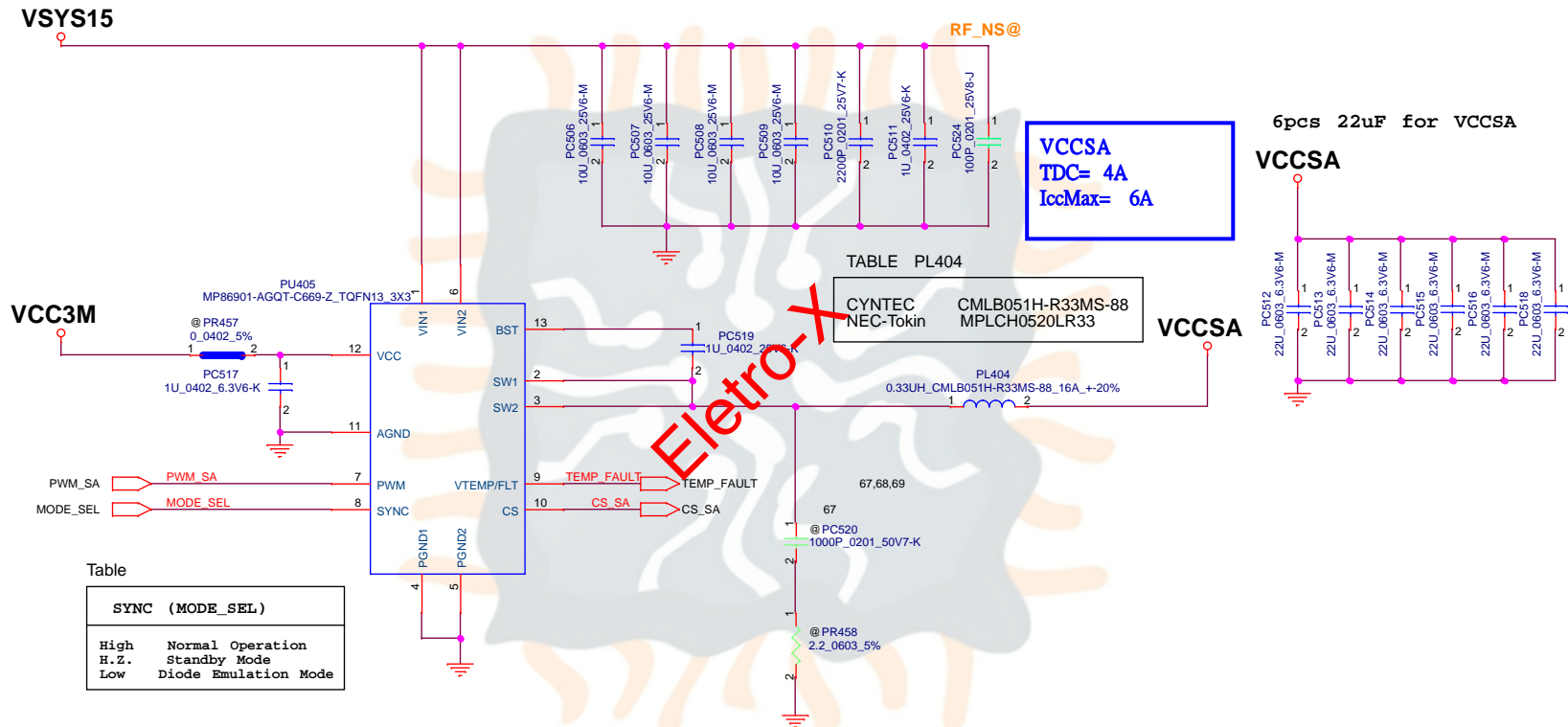


Table PD701,PD702

ROHM: RB521CM-30T2R
ONSEMI: NSR0140P2T5G
NXP: PMEG4002EL

Table PL701

TOKO: FDS0420-H-1R0M
Cyntec: CMLB042T-1R0MS
TDK: SPM4020T-1R0M-LR


TABLE : NB695 MODE M1 (0 to GND)

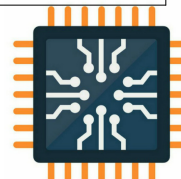
LP#	C1	C0	VOUT
0	X	X	0.000V
1	0	0	0.850V
1	0	1	0.875V
1	1	0	0.950V
1	1	1	0.975V

← SLP_S0#
← LOGIC

If remote sense is used, PR712 should be 0 ohm and PR708 be 6.8 ohm.
If not , PR712 should be not stuffed and PR708 = 0 ohm.

Differential Lines

Security Classification	LC Future Center Secret Data			Title	
Issued Date		Deciphered Date		DC/DC VCCCPUIO(NB695)	
<p>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.</p>					Rev 0.01
Size	Document Number	Yoda-2			
Custom					
Date:	Friday, November 03, 2017			Sheet	72 of 86



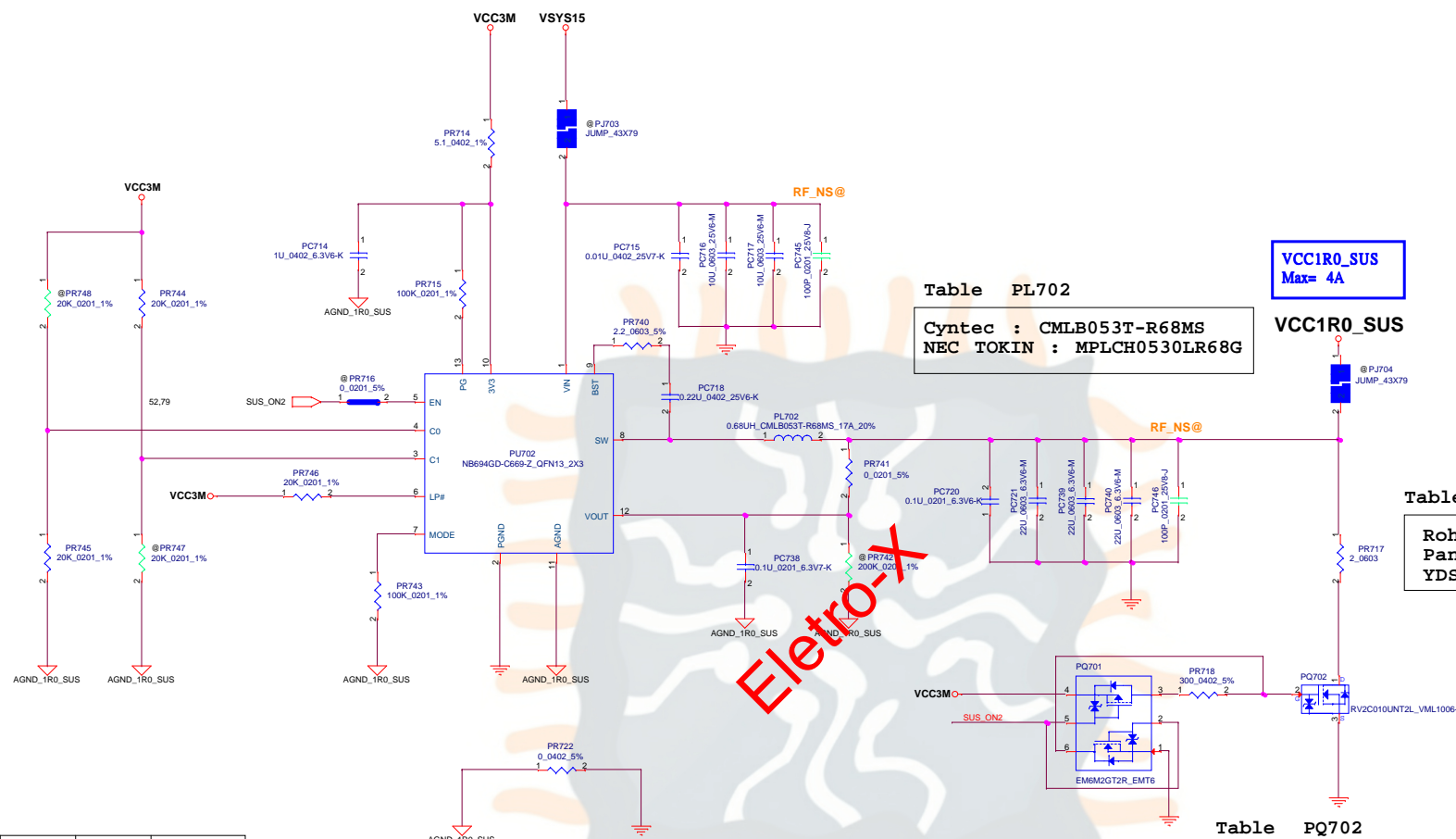


Table PL702
Cyntec : CMLB053T-R68MS
NEC TOKIN : MPLCH0530LR68G

VCC1R0_SUS
Max= 4A


VCC1R0_SUS

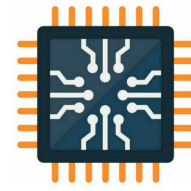
Table PR717 0603 SIZE
Rohm:ESR03EZPJ2R0
Pana:ERJPA3J2R0V
YDS:RN73S2CL-2R00-F

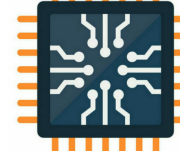
Table PQ702
ROHM : RV2C010UNT2L
Toshiba : SSM3K56ACT

Control Bit Logics				
	LP#	C1	C0	VOUT(V)
V1.0A	0	X	X	0V
	1	0	0	0.85V
	1	0	1	0.95V
	1	1	0	1V
	1	1	1	1.05V

← Default Value

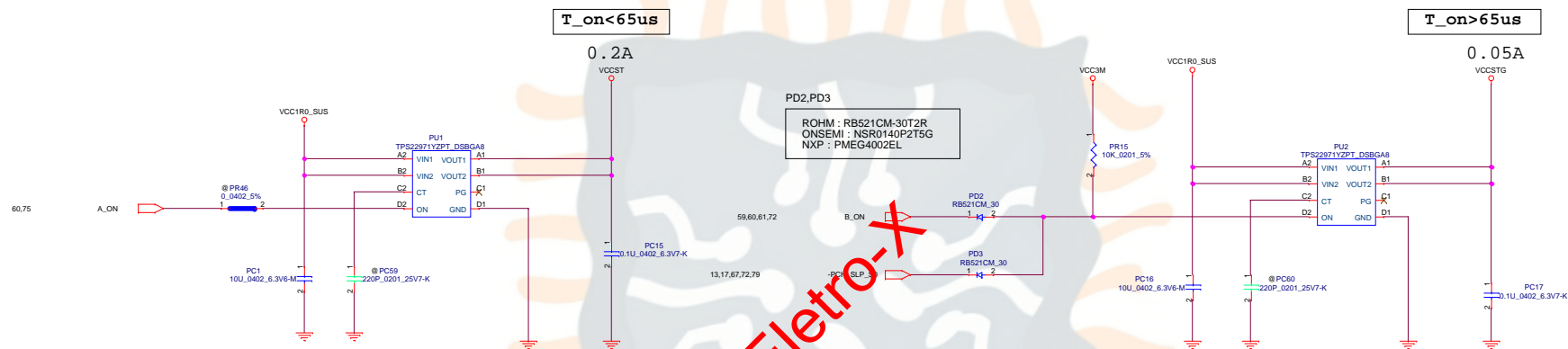
Security Classification		LC Future Center Secret Data		Title			
Issued Date		Deciphered Date		DC/DC VCC1R0_SUS(NB694)			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.							
Size		Document		Number		Rev	
Custom				Yoda-2		0.01	
Date		Friday, November 03, 2017		Sheet		73 of 86	

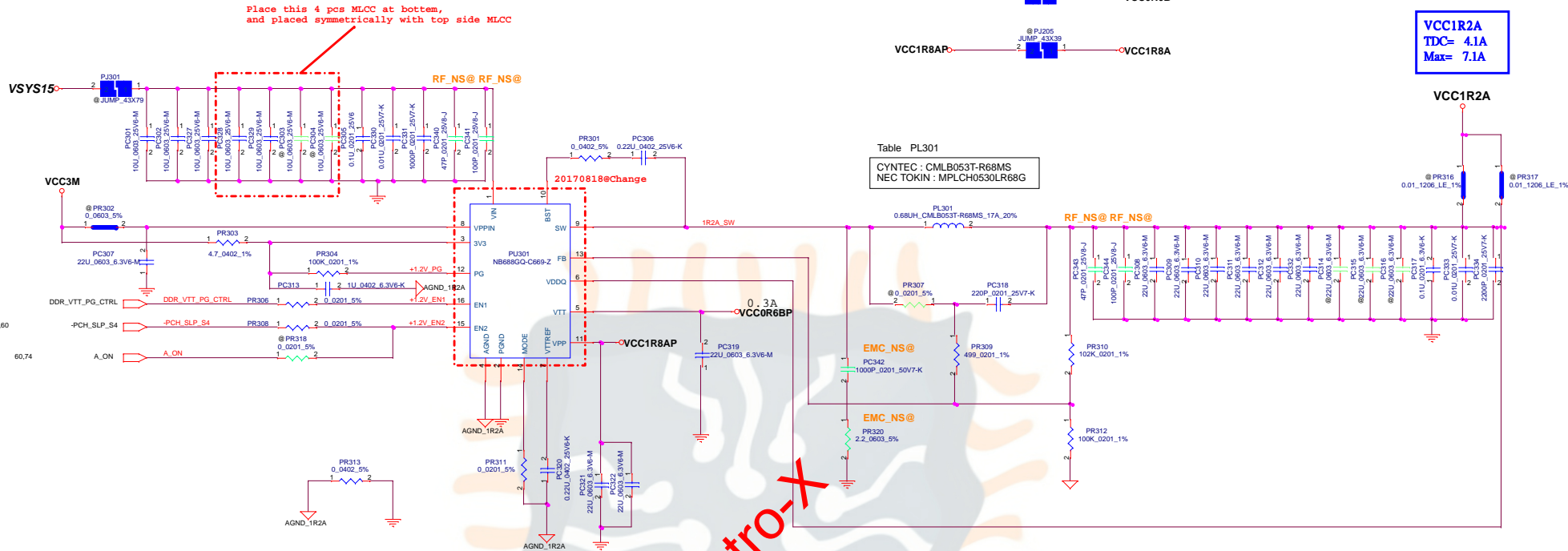




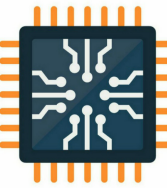
Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		LOAD SW VCCST & VCCSTG	
Size		Document Number		Rev	
Custom		Yoda-2		0.01	
Date:		Friday, November 03, 2017		Sheet 74 of 86	

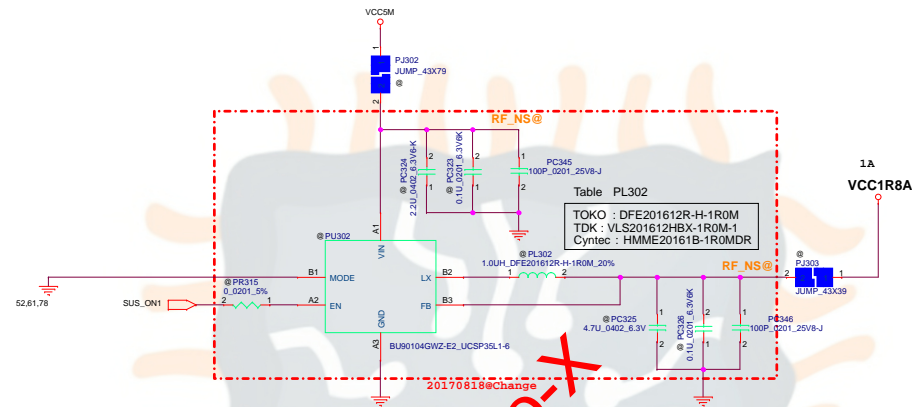
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.





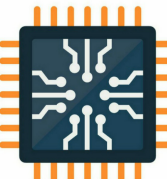
Security Classification	LC Future Center Secret Data		Title
Issued Date	2016/12/29	Deciphered Date	2014/12/31
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			DC/DC VCC1R2A(NB687)
Size	Document Number	Customer	Yoda-2
Date	Friday, November 03, 2017	Sheet	75 of 86

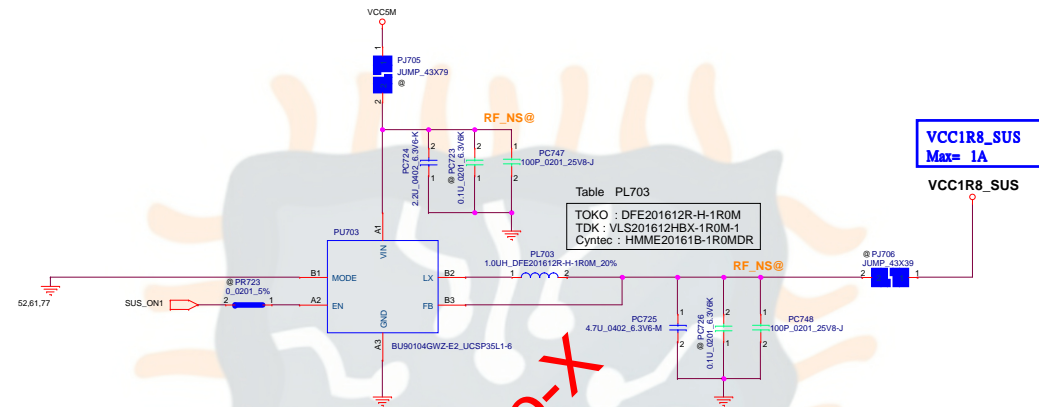




Component Name	EVT	FVT	SIT
PU302	ASM	ASM	NO_ASM
PC324	ASM	ASM	NO_ASM
PC323	NO_ASM	NO_ASM	NO_ASM
PC345	NO_ASM	NO_ASM	NO_ASM
PL302	ASM	ASM	NO_ASM
PC325	ASM	ASM	NO_ASM
PC326	NO_ASM	NO_ASM	NO_ASM
PC346	NO_ASM	NO_ASM	NO_ASM
PR315	ASM	ASM	NO_ASM
PJ302	ASM	ASM	NO_ASM
PJ303	ASM	ASM	NO_ASM

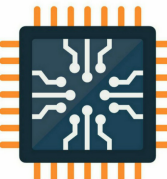
Security Classification	LC Future Center Secret Data		Title
Issued Date	Deciphered Date		DCDC VCC1R8A(BU90104GWZ)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			
Size	Document Number	Yoda-2	
Custom		Rev 0.01	
Date:	Friday, November 03, 2017	Sheet	77 of 86





Eletr-X

Security Classification		LC Future Center Secret Data		Title	
Issued Date		Deciphered Date		DC/DC VCC1R8_SUS/BU90104GWZ	
<div>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.</div>					
Size	Document Number	Yoda-2		Rev	0.01
Custom					
Date:		Friday, November 03, 2017		Sheet	78 of 86



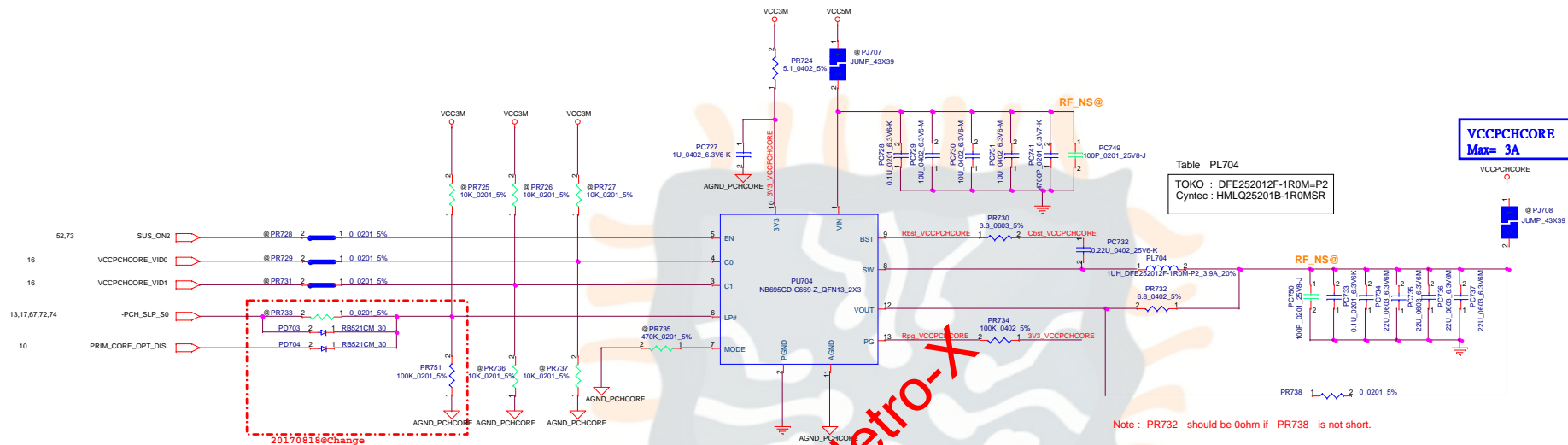


TABLE : NB695 MODE M2 (Float)

LP#	C1	C0	VOUT
0	X	X	0.700V
1	0	0	0.850V
1	0	1	0.900V
1	1	0	0.950V
1	1	1	1.000V

← SLP_S0#

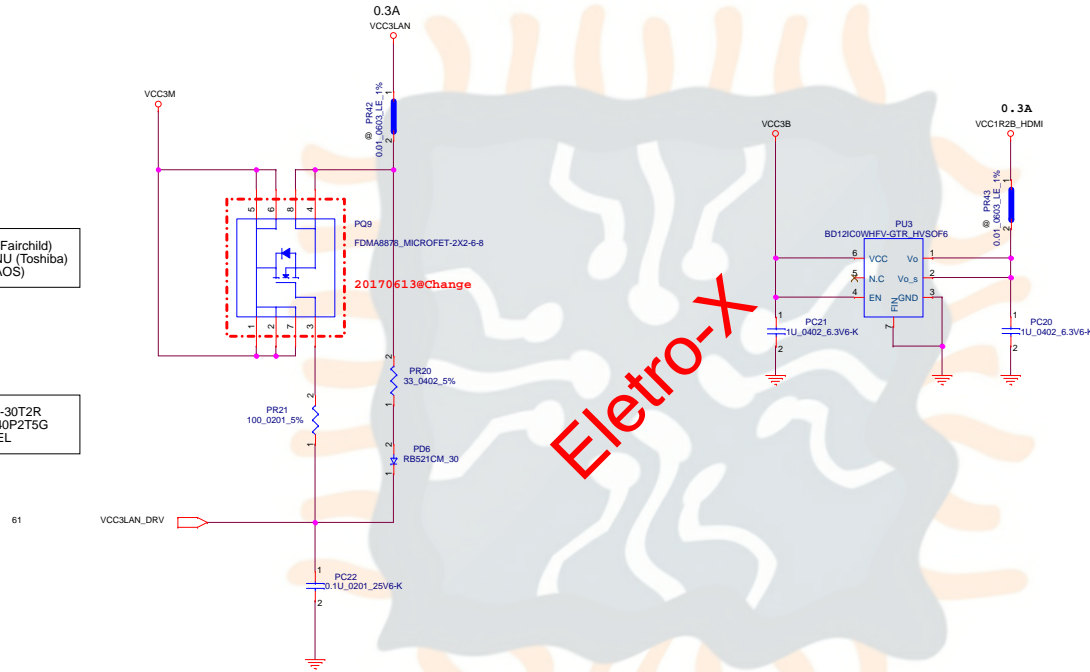
← DEFAULT

PQ9

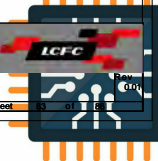
Main: FDMA8878 (Fairchild)
2nd : SSM6K504NU (Toshiba)
3rd : AON2420 (AOS)

PD6

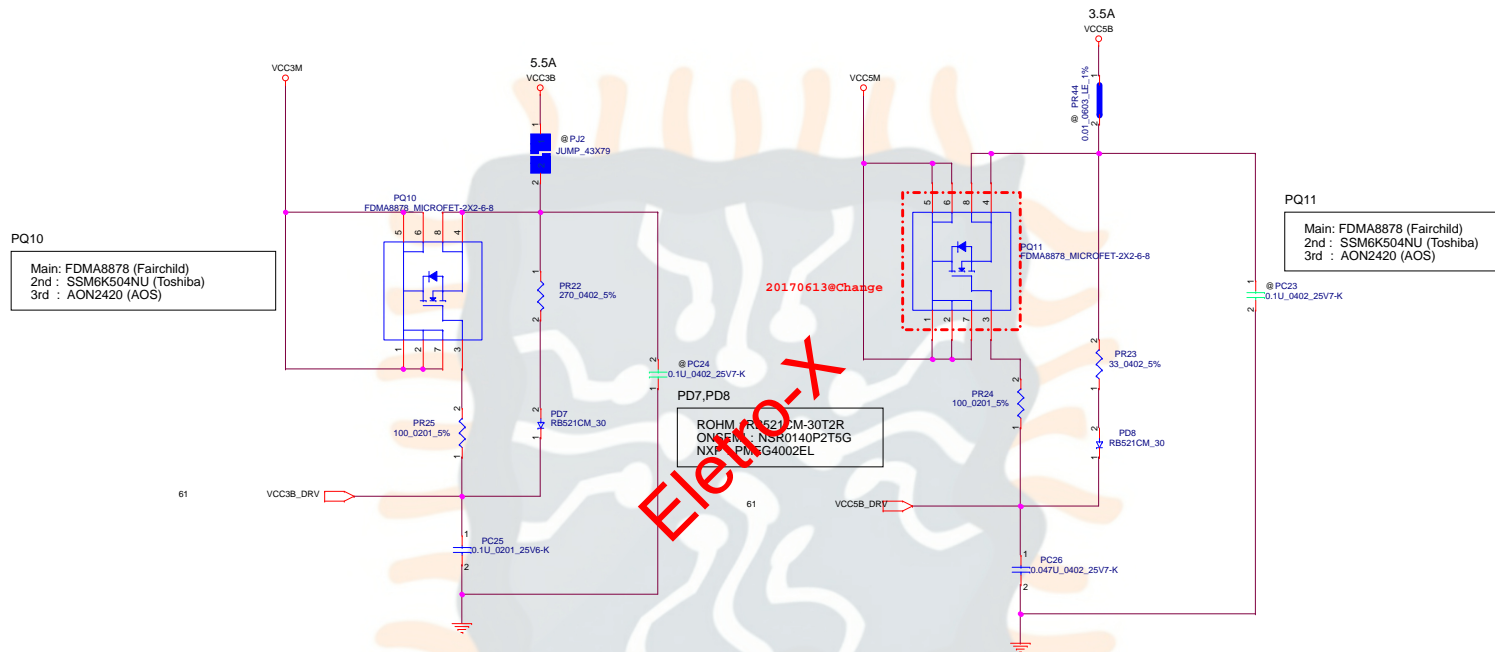
ROHM : RB521CM-30T2R
ONSEMI : NSR0140P2T5G
NXP : PMEG4002EL



Security Classification		LC Future Center Secret Data		Title
Issued Date	2012/11/01	Deciphered Date	2013/12/31	LOAD SW LAN/VCC1R2B_HDMI
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.				
Size	Document Number	Yoda-2		
Customer	Friday, November 03, 2017			



Eleetro-X



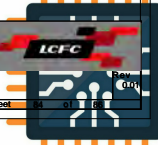
PQ10
Main: FDMA8878 (Fairchild)
2nd : SSM6K504NU (Toshiba)
3rd : AON2420 (AOS)

PQ11
Main: FDMA8878 (Fairchild)
2nd : SSM6K504NU (Toshiba)
3rd : AON2420 (AOS)

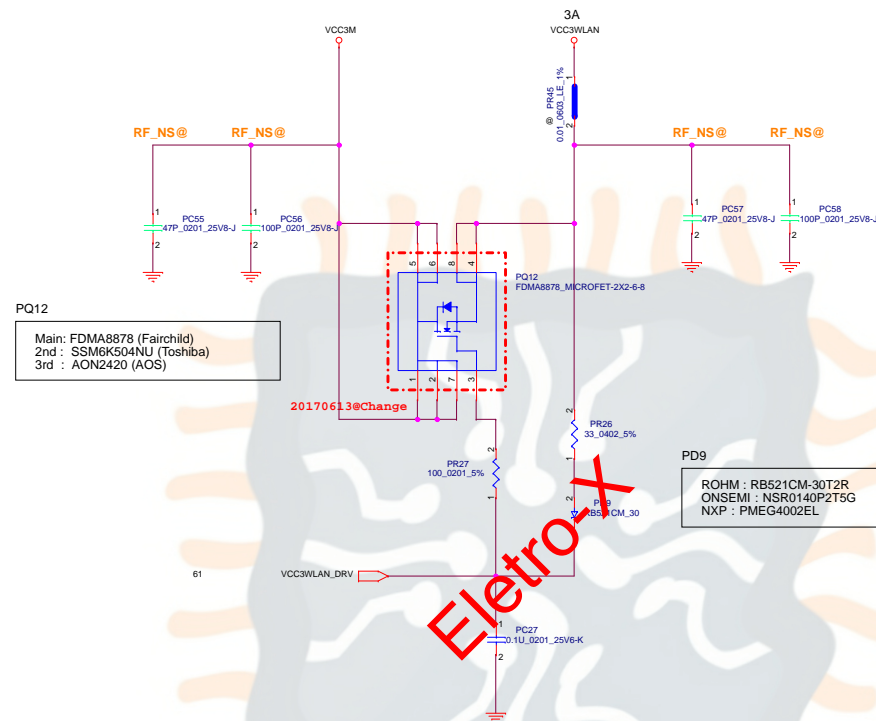
PD7,PD8
ROHM RM6501CM-30T2R
ON Semiconductor NSR0140P2T5G
NXP PM65G4002EL

Security Classification	LC Future Center Secret Data	
Issued Date	2012/11/01	Deciphered Date
		2013/12/31
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.		

Title	
LOAD SW B	
Size	Document Number
Custom	Yoda-2
Date:	Friday, November 03, 2017

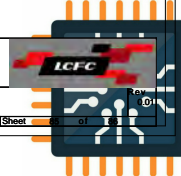


ELECTRO-X



Security Classification	LC Future Center Secret Data		
Issued Date	2012/11/01	Deciphered Date	2013/12/31
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			

Title		LOAD SW WLAN	
Size	Document Number	Yoda-2	
Custom			
Date:	Friday, November 03, 2017	Sheet	01



Eletron-2

PTH FOR SCREW HOLE

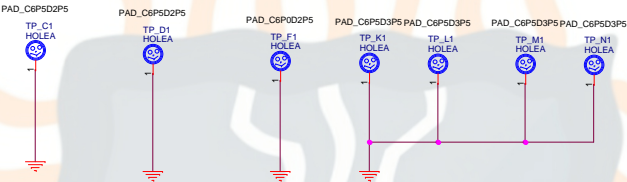
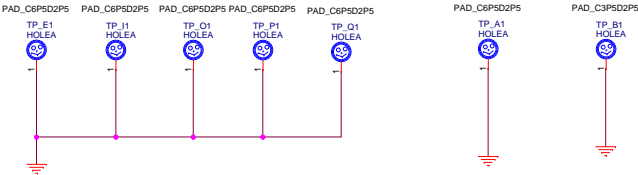
Value	Hole Dia	Pad Dia		QTY
		TOP	BOTTOM	
TESTPIN_2P5_01	2.5	6.5	6.5	5
TESTPIN_2P5_02	2.5	6.5	6.0	1
TESTPIN_2P5_03	2.5	6.0	6.0	1
TESTPIN_2P5_04	2.5	6.5	5.6	1
TESTPIN_2P5_05	2.5	6.5	sha	1
TESTPIN_2P5_06	2.5	6.5	shb	1
TESTPIN_3P5_01	3.5	6.5	6.5	4
STUD_3P3_01 (SM20L66621)	3.3	6.5	6.5	3

Need to Apply New PCBFootprint

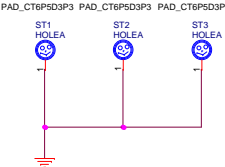
Need to Apply New PCBFootprint

Will Check with ME

NPTH

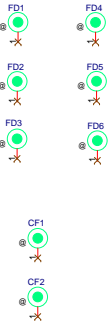


STUD STUD STUD



FID
Board Area

FID
Component Area



Security Classification	LC Future Center Secret Data		Title
Issued Date	2017/02/17	Deciphered Date	2014/07/01
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FUTURE CENTER, AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY LC FUTURE CENTER. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FUTURE CENTER.			
Size	Document Number	Yoda-2	
Date:	Friday, November 03, 2017	Sheet	

